

54F/74F151A 8-Input Multiplexer

General Description

The 'F151A is a high-speed 8-input digital multiplexer. It provides in one package the ability to select one line of data from up to eight sources. The 'F151A can be used as a

universal function generator to generate any logic function of four variables. Both assertion and negation outputs are provided.

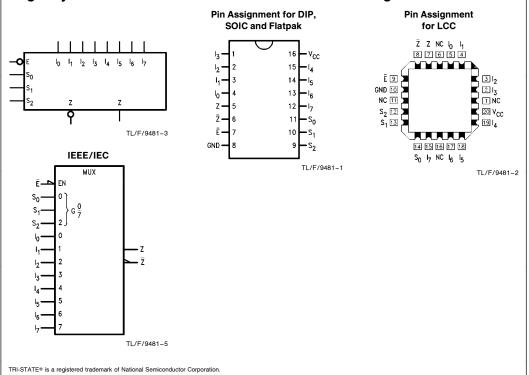
Commercial	Military	Package Number	Package Description		
74F151APC		N16E	16-Lead (0.300" Wide) Molded Dual-In-Line		
	54F151ADM (Note 2)	J16A	16-Lead Ceramic Dual-In-Line		
74F151ASC (Note 1)		M16A	16-Lead (0.150" Wide) Molded Small Outline, JEDEC		
74F151ASJ (Note 1)		M16D	16-Lead (0.300" Wide) Molded Small Outline, EIAJ		
	54F151AFM (Note 2)	W16A	16-Lead Cerpack		
	54F151ALM (Note 2)	E20A	20-Lead Ceramic Leadless Chip Carrier, Type C		

Note 1: Devices also available in 13" reel. Use suffix = SCX and SJX.

 $\textbf{Note 2:} \ \textbf{Military grade device with environmental and burn-in processing.} \ \textbf{Use suffix} = \textbf{DQMB, FMQB and LMQB.}$

Logic Symbols

Connection Diagrams



Unit Loading/Fan Out

		54F/74F			
Pin Names	Description	U.L. HIGH/LOW	Input I _{IH} /I _{IL} Output I _{OH} /I _{OL}		
I ₀ -I ₇	Data Inputs	1.0/1.0	20 μA/-0.6 mA		
S ₀ -S ₂	Select Inputs	1.0/1.0	$20 \mu\text{A}/-0.6\text{mA}$		
Ē	Enable Input (Active LOW)	1.0/1.0	$20 \mu\text{A}/-0.6\text{mA}$		
Z	Data Output	50/33.3	-1 mA/20 mA		
Z	Inverted Data Output	50/33.3	-1 mA/20 mA		

Functional Description

The 'F151A is a logic implementation of a single pole, 8-position switch with the switch position controlled by the state of three Select inputs, $S_0,\,S_1,\,S_2.$ Both assertion and negation outputs are provided. The Enable input (\overline{E}) is active LOW. When it is not activated, the negation output is HIGH and the assertion output is LOW regardless of all other inputs. The logic function provided at the output is:

$$\begin{split} Z &= \overline{E} \bullet (I_0 \, \overline{S}_2 \, \overline{S}_1 \, \overline{S}_0 + I_1 \, \overline{S}_2 \, \overline{S}_1 \, S_0 + I_2 \, \overline{S}_2 \, S_1 \, \overline{S}_0 + I_3 \, \overline{S}_2 \, S_1 \, \overline{S}_0 + I_4 \, S_2 \, \overline{S}_1 \, \overline{S}_0 + I_5 \, S_2 \, \overline{S}_1 \, S_0 + I_6 \, S_2 \, S_1 \, \overline{S}_0 + I_7 \, S_2 \, S_1 \, S_0) \end{split}$$

The 'F151A provides the ability, in one package, to select from eight sources of data or control information. By proper manipulation of the inputs, the 'F151A can provide any logic function of four variables and its negation.

Truth Table

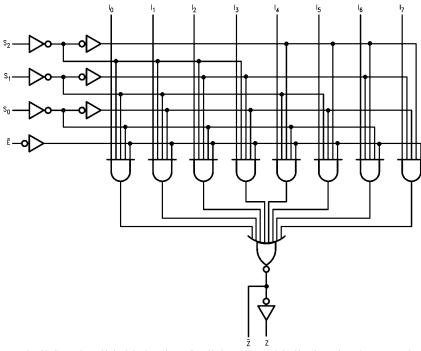
	Inp	Outputs			
Ē	S ₂	S ₁	S_0	Z	z
Н	X	X	Х	Н	L
L	L	L	L	Īo	I ₀
L	L	L	Н	Ī ₁	11
L	L	Н	L	Ī ₂	l ₂
L	L	Н	Н	Ī ₃	l ₃
L	Н	L	L	Ī ₄	14
L	Н	L	Н	Ī ₅	l ₅
L	Н	Н	L	Ī ₆	I ₆
L	Н	Н	Н	Ī ₇	l ₇

H = HIGH Voltage Level

L = LOW Voltage Level

X = Immaterial

Logic Diagram



TI /F/9481-

Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

 $\begin{array}{lll} \mbox{Storage Temperature} & -65^{\circ}\mbox{C to} + 150^{\circ}\mbox{C} \\ \mbox{Ambient Temperature under Bias} & -55^{\circ}\mbox{C to} + 125^{\circ}\mbox{C} \\ \mbox{Junction Temperature under Bias} & -55^{\circ}\mbox{C to} + 175^{\circ}\mbox{C} \\ \mbox{Plastic} & -55^{\circ}\mbox{C to} + 150^{\circ}\mbox{C} \\ \end{array}$

V_{CC} Pin Potential to

Ground Pin -0.5V to +7.0V Input Voltage (Note 2) -0.5V to +7.0V Input Current (Note 2) -30 mA to +5.0 mA

Voltage Applied to Output in HIGH State (with $V_{CC} = 0V$)

 $\begin{array}{lll} \text{Standard Output} & -0.5 \text{V to V}_{\text{CC}} \\ \text{TRI-STATE} \text{® Output} & -0.5 \text{V to } +5.5 \text{V} \end{array}$

Current Applied to Output

in LOW State (Max) twice the rated I_{OL} (mA)

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

Recommended Operating Conditions

Free Air Ambient Temperature

Military $-55^{\circ}\text{C to} + 125^{\circ}\text{C}$ Commercial $0^{\circ}\text{C to} + 70^{\circ}\text{C}$

Supply Voltage

Military + 4.5V to + 5.5V Commercial + 4.5V to + 5.5V

DC Electrical Characteristics

Symbol	Parameter		54F/74F			Units	v _{cc}	Conditions	
Symbol			Min	Тур	Max	Oilles	VCC	Conditions	
V _{IH}	Input HIGH Voltage		2.0			V		Recognized as a HIGH Signal	
V_{IL}	Input LOW Voltage				0.8	V		Recognized as a LOW Signal	
V_{CD}	Input Clamp Diode Vo	oltage			-1.2	V	Min	$I_{\text{IN}} = -18 \text{mA}$	
V _{OH}	Output HIGH Voltage	54F 10% V _{CC} 74F 10% V _{CC} 74F 5% V _{CC}	2.5 2.5 2.7			V	Min	$I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$	
V _{OL}	Output LOW Voltage	54F 10% V _{CC} 74F 10% V _{CC}			0.5 0.5	٧	Min	$I_{OL} = 20 \text{ mA}$ $I_{OL} = 20 \text{ mA}$	
l _{IH}	Input HIGH Current	54F 74F			20.0 5.0	μΑ	Max	V _{IN} = 2.7V	
I _{BVI}	Input HIGH Current Breakdown Test	54F 74F			100 7.0	μΑ	Max	V _{IN} = 7.0V	
I _{CEX}	Output HIGH Leakage Current	54F 74F			250 50	μΑ	Max	$V_{OUT} = V_{CC}$	
V_{ID}	Input Leakage Test	74F	4.75			٧	0.0	$I_{\text{ID}} = 1.9 \mu\text{A}$ All Other Pins Grounded	
l _{OD}	Output Leakage Circuit Current	74F			3.75	μΑ	0.0	V _{IOD} = 150 mV All Other Pins Grounded	
I _{IL}	Input LOW Current				-0.6	mA	Max	V _{IN} = 0.5V	
I _{OS}	Output Short-Circuit Current		-60		-150	mA	Max	V _{OUT} = 0V	
I _{CC}	Power Supply Curren		13.5	21.0	mA	Max	V _O = HIGH		

AC Electrical Characteristics

		$74F$ $T_A = +25^{\circ}C$ $V_{CC} = +5.0V$ $C_L = 50 \text{ pF}$			54F T _A , V _{CC} = Mil C _L = 50 pF		74F T _A , V _{CC} = Com C _L = 50 pF		Units
Symbol	Parameter								
		Min	Тур	Max	Min	Max	Min	Max	
t _{PLH}	Propagation Delay S_n to \overline{Z}	4.0 3.2	6.2 5.2	9.0 7.5	3.5 3.0	11.5 8.0	3.5 3.2	9.5 7.5	ns
t _{PLH}	Propagation Delay S _n to Z	4.5 4.0	7.5 6.2	10.5 9.0	4.5 4.0	13.5 9.5	4.5 4.0	12.0 9.0	ns
t _{PLH}	Propagation Delay E to Z	3.0 3.0	4.7 4.4	6.1 6.0	3.0 2.5	7.5 6.5	3.0 2.5	7.0 6.0	ns
t _{PLH}	Propagation Delay E to Z	5.0 3.5	7.0 5.3	9.5 7.0	4.0 3.0	12.0 8.0	4.0 3.0	10.5 7.5	ns
t _{PLH}	Propagation Delay I_n to \overline{Z}	3.0 1.5	4.8 2.5	6.5 4.0	2.5 1.5	7.5 6.0	3.0 1.5	7.0 5.0	ns
t _{PLH}	Propagation Delay In to Z	3.0 3.7	4.8 5.5	6.5 7.0	2.5 3.5	8.5 9.0	2.5 3.7	7.5 7.5	ns

Ordering Information

<u>74F 151A S C</u> Temperature Range Family -74F = Commercial 54F = Military Device Type Package Code

P = Plastic DIP

D = Ceramic DIP

F = Flatpak

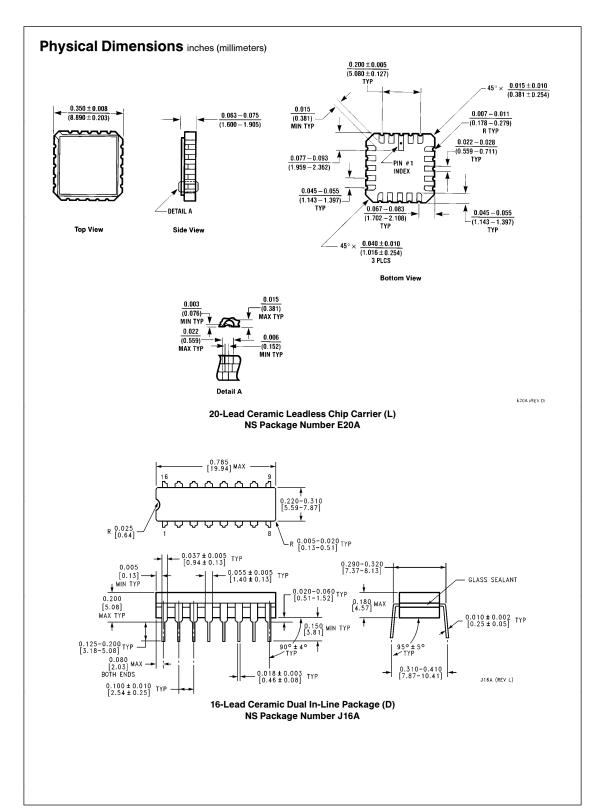
L = Leadless Chip Carrier (LCC)

S = Small Outline SOIC JEDEC SJ = Small Outline SOIC EIAJ

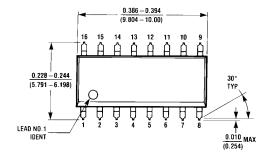
Special Variations
QB = Military grade device with environmental and burn-in processing

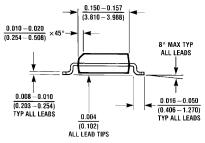
X = Devices shipped in 13" reel

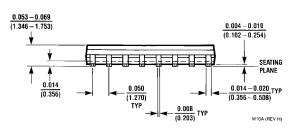
Temperature Range C = Commercial (0°C to +70°C) M = Military (-55°C to +125°C)



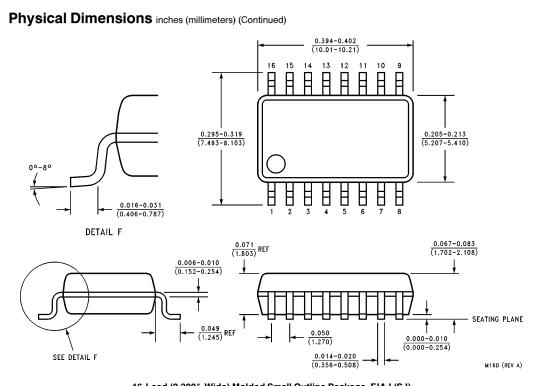
Physical Dimensions inches (millimeters) (Continued)



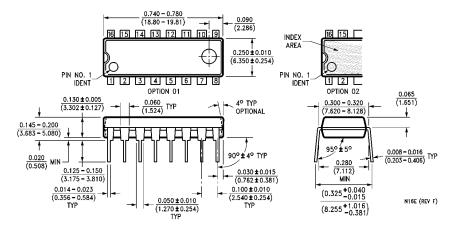




16-Lead (0.150" Wide) Molded Small Outline Package, JEDEC (S) NS Package Number M16A

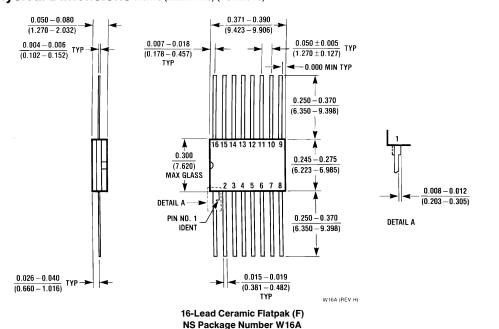


16-Lead (0.300" Wide) Molded Small Outline Package, EIAJ (SJ) NS Package Number M16D



16-Lead (0.300" Wide) Molded Dual In-Line Package (P) NS Package Number N16E

Physical Dimensions inches (millimeters) (Continued)



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