

Product Overview

74LVC125: Low Voltage Quad Non-Inverting Buffer, 3-State

For complete documentation, see the data sheet.

The 74LVC125A is a high performance, non-inverting quad buffer operating from a 1.2 to 3.6 V supply. High impedance TTL compatible inputs significantly reduce current loading to input drivers while TTL compatible outputs offer improved switching noise performance. A VI specification of 5.5 V allows 74LVC125A inputs to be safely driven from 5.0 V devices. The 74LVC125A is suitable for memory address driving and all TTL level bus oriented transceiver applications.

Current drive capability is 24 mA at the outputs. The Output Enable (OE \bar) inputs, when HIGH, disable the outputs by placing them in a HIGH Z condition.

Features

- Designed for 1.2 to 3.6 V V_{CC} Operation
- 5.0 V Tolerant - Interface Capability With 5.0 V TTL Logic
- Supports Live Insertion and Withdrawal
- I_{OFF} Specification Guarantees High Impedance When $V_{CC} = 0$ V
- 24 mA Output Sink and Source Capability
- Near Zero Static Supply Current in all Three Logic States (10 μ A) Substantially Reduces System Power Requirements
- Latchup Performance Exceeds 250 mA
- ESD Performance: Human Body Model >2000 V; Machine Model >200 V
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Part Electrical Specifications

Product	Compliance	Status	Channels	Output	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_O Max (mA)	Package Type
74LVC125ADR2G	Pb-free	Active	4	3-State	1.2	3.6	6	24	SOIC-14
	Halide free								
74LVC125ADTR2G	Pb-free	Active	4	3-State	1.2	3.6	6	24	TSSOP-14
	Halide free								

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