

Product Overview

74LVC574: Low Voltage CMOS Octal D Flip-Flop Flow Through Pinout

For complete documentation, see the data sheet.

The 74LVC574A is a high performance, non-inverting octal D-type flip-flop operating from a 1.2 to 3.6V supply. High impedance TTL compatible inputs significantly reduce current loading to input drivers while TTL compatible outputs offer improved switching noise performance. A V_I specification of 5.5V allows 74LVC574A inputs to be safely driven from 5V devices.

The 74LVC574A consists of 8 edge-triggered flip-flops with individual D-type inputs and 3-state true outputs. The buffered clock and buffered Output Enable (OEbar) are common to all flip-flops. The eight flip-flops will store the state of individual D inputs that meet the setup and hold time requirements on the LOW-to-HIGH Clock (CP) transition. With the OEbar LOW, the contents of the eight flip-flops are available at the outputs. When the OEbar is HIGH, the outputs go to the high impedance state. The OEbar input level does not affect the operation of the flip-flops. The 74LVC574A flow through design facilitates easy PC board layout.

Features

- Designed for 1.2 to 3.6 V V_{CC} Operation
- 5 V Tolerant - Interface Capability With 5 V TTL Logic
- Supports Live Insertion and Withdrawal
- I_{OFF} Specification Guarantees High Impedance When $V_{CC} = 0$ V
- 24 mA Output Sink and Source Capability
- Near Zero Static Supply Current in All Three Logic States (10 μ A) Substantially Reduces System Power Requirements
- Latch-up Performance Exceeds 250 mA
- ESD Performance: Human Body Model >2000 V; Machine Model >200 V
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Part Electrical Specifications

Product	Compliance	Status	Type	Channels	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_O Max (mA)	Package Type
74LVC574ADTR2G	Pb-free Halide free	Active	D-Type	8	1.2	3.6	7	24	TSSOP 20 LEAD

For more information please contact your local sales support at www.onsemi.com.

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