

# 74VHC175 Quad D-Type Flip-Flop

#### Features

- High Speed: f<sub>MAX</sub> = 210MHz (Typ.) at V<sub>CC</sub> = 5V
- Low power dissipation: I<sub>CC</sub> = 4µA (Max.) at T<sub>A</sub> = 25°C
- High noise immunity: V<sub>NIH</sub> = V<sub>NIL</sub> = 28% V<sub>CC</sub> (Min.)
- Power down protection is provided on all inputs
- Low noise: V<sub>OLP</sub> = 0.8V (Max.)
- Pin and function compatible with 74HC175

### **General Description**

The VHC175 is an advanced high-speed CMOS device fabricated with silicon gate CMOS technology. It achieves the high-speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The VHC175 is a high-speed quad D-type flip-flop. The device is useful for general flip-flop requirements where clock and clear inputs are common. The information on the D inputs is stored during the LOW-to-HIGH clock transition. Both true and complemented outputs of each flip-flop are provided. A Master Reset input resets all flip-flops, independent of the Clock or D inputs, when LOW.

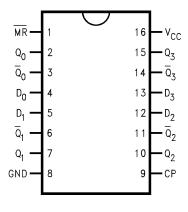
An input protection circuit insures that 0V to 7V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5V to 3V systems and two supply systems such as battery backup. This circuit prevents device destruction due to mismatched supply and input voltages.

| Ordering | Inform | nation |
|----------|--------|--------|
| o ao mg  |        |        |

| Order Number | Package<br>Number | Package Description  |
|--------------|-------------------|--|
| 74VHC175M    | M16A              | 16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow |
| 74VHC175SJ   | M16D              | 16-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide                |
| 74VHC175MTC  | MTC16             | 16-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide  |

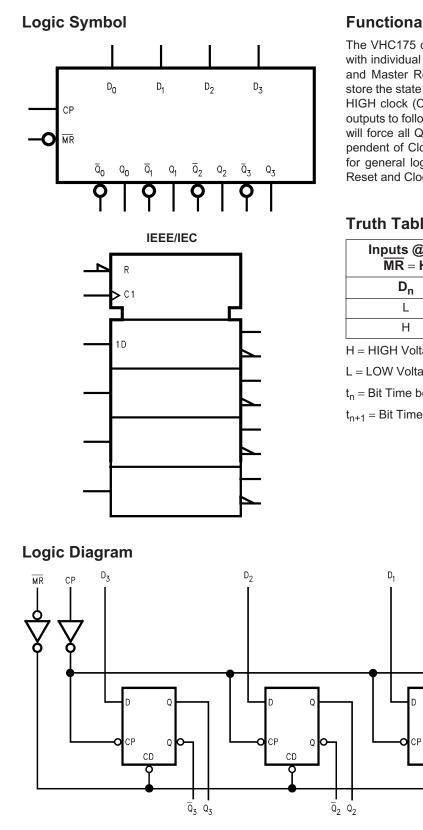
Surface mount packages are also available on Tape and Reel. Specify by appending the suffix letter "X" to the ordering number.

## **Connection Diagram**



#### **Pin Description**

| Pin Names                           | Description        |
|-------------------------------------|--------------------|
| D <sub>0</sub> D <sub>3</sub>       | Data Inputs        |
| CP                                  | Clock Pulse Input  |
| MR                                  | Master Reset Input |
| Q <sub>0</sub> -Q <sub>3</sub>      | True Outputs       |
| $\overline{Q}_{0}-\overline{Q}_{3}$ | Complement Outputs |



## **Functional Description**

The VHC175 consists of four edge-triggered D flip-flops with individual D inputs and Q and  $\overline{Q}$  outputs. The Clock and Master Reset are common. The four flip-flops will store the state of their individual D inputs on the LOW-to-HIGH clock (CP) transition, causing individual Q and  $\overline{\mathsf{Q}}$ outputs to follow. A LOW input on the Master Reset ( $\overline{MR}$ ) will force all Q outputs LOW and  $\overline{Q}$  outputs HIGH independent of Clock or Data inputs. The VHC175 is useful for general logic applications where a common Master Reset and Clock are acceptable.

## **Truth Table**

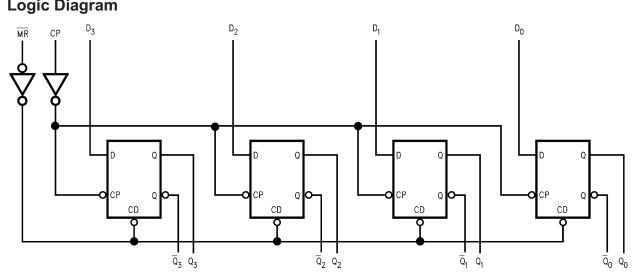
| Inputs @ t <sub>n</sub> ,<br>MR = H | Outputs        | s @ t <sub>n+1</sub> |
|-------------------------------------|----------------|----------------------|
| D <sub>n</sub>                      | Q <sub>n</sub> | $\overline{Q}_{n}$   |
| L                                   | L              | Н                    |
| Н                                   | Н              | L                    |

H = HIGH Voltage Level

L = LOW Voltage Level

t<sub>n</sub> = Bit Time before Clock Pulse

tn+1 = Bit Time after Clock Pulse



Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

| Symbol           | Parameter                                | Rating                          |
|------------------|--|---------------------------------|
| V <sub>CC</sub>  | Supply Voltage                           | -0.5V to +7.0V                  |
| V <sub>IN</sub>  | DC Input Voltage                         | -0.5V to +7.0V                  |
| V <sub>OUT</sub> | DC Output Voltage                        | –0.5V to V <sub>CC</sub> + 0.5V |
| I <sub>IK</sub>  | Input Diode Current                      | –20mA                           |
| I <sub>OK</sub>  | Output Diode Current                     | ±20mA                           |
| I <sub>OUT</sub> | DC Output Current                        | ±25mA                           |
| I <sub>CC</sub>  | DC V <sub>CC</sub> / GND Current         | ±50mA                           |
| T <sub>STG</sub> | Storage Temperature                      | –65°C to +150°C                 |
| TL               | Lead Temperature (Soldering, 10 seconds) | 260°C                           |

## Recommended Operating Conditions<sup>(1)</sup>

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

| Symbol                          | Parameter                 | Rating                |
|---------------------------------|---------------------------|-----------------------|
| V <sub>CC</sub>                 | Supply Voltage            | 2.0V to +5.5V         |
| V <sub>IN</sub>                 | Input Voltage             | 0V to +5.5V           |
| V <sub>OUT</sub>                | Output Voltage            | 0V to V <sub>CC</sub> |
| T <sub>OPR</sub>                | Operating Temperature     | –40°C to +85°C        |
| t <sub>r</sub> , t <sub>f</sub> | Input Rise and Fall Time, |                       |
|                                 | $V_{CC} = 3.3V \pm 0.3V$  | 0ns/V ~ 100ns/V       |
|                                 | $V_{CC} = 5.0V \pm 0.5V$  | 0ns/V ~ 20ns/V        |

Note:

1. Unused inputs must be held HIGH or LOW. They may not float.

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## **DC Electrical Characteristics**

|                 |                             |                     |                        |                         | т                     | A = 25° | С                     |                       | 40°C to<br>5°C        |       |
|-----------------|-----------------------------|---------------------|------------------------|-------------------------|-----------------------|---------|-----------------------|-----------------------|-----------------------|-------|
| Symbol          | Parameter                   | V <sub>CC</sub> (V) | Con                    | ditions                 | Min.                  | Тур.    | Max.                  | Min.                  | Max.                  | Units |
| V <sub>IH</sub> | HIGH Level Input            | 2.0                 |                        |                         | 1.50                  |         |                       | 1.50                  |                       | V     |
|                 | Voltage                     | 3.0–5.5             | 1                      |                         | 0.7 x V <sub>CC</sub> |         |                       | 0.7 x V <sub>CC</sub> |                       |       |
| V <sub>IL</sub> | LOW Level Input             | 2.0                 |                        |                         |                       |         | 0.50                  |                       | 0.50                  | V     |
|                 | Voltage                     | 3.0–5.5             | 1                      |                         |                       |         | 0.3 x V <sub>CC</sub> |                       | 0.3 x V <sub>CC</sub> | 1     |
| V <sub>OH</sub> | HIGH Level                  | 2.0                 |                        | I <sub>OH</sub> = -50µA | 1.9                   | 2.0     |                       | 1.9                   |                       | V     |
|                 | Output Voltage              | 3.0                 | or V <sub>IL</sub>     |                         | 2.9                   | 3.0     |                       | 2.9                   |                       |       |
|                 |                             | 4.5                 | 1                      |                         | 4.4                   | 4.5     |                       | 4.4                   |                       |       |
|                 |                             | 3.0                 | 1                      | $I_{OH} = -4mA$         | 2.58                  |         |                       | 2.48                  |                       |       |
|                 |                             | 4.5                 | 1                      | I <sub>OH</sub> = -8mA  | 3.94                  |         |                       | 3.80                  |                       |       |
| V <sub>OL</sub> | LOW Level                   | 2.0                 | $V_{IN} = V_{IH}$      | I <sub>OL</sub> = 50μA  |                       | 0.0     | 0.1                   |                       | 0.1                   | V     |
|                 | Output Voltage              | 3.0                 | or V <sub>IL</sub>     |                         |                       | 0.0     | 0.1                   |                       | 0.1                   |       |
|                 |                             | 4.5                 | 1                      |                         |                       | 0.0     | 0.1                   |                       | 0.1                   |       |
|                 |                             | 3.0                 | 1                      | $I_{OL} = 4mA$          |                       |         | 0.36                  |                       | 0.44                  |       |
|                 |                             | 4.5                 | 1                      | I <sub>OL</sub> = 8mA   |                       |         | 0.36                  |                       | 0.44                  |       |
| I <sub>IN</sub> | Input Leakage<br>Current    | 0–5.5               | V <sub>IN</sub> = 5.5V | or GND                  |                       |         | ±0.1                  |                       | ±1.0                  | μA    |
| I <sub>CC</sub> | Quiescent<br>Supply Current | 5.5                 | $V_{IN} = V_{CC}$      | or GND                  |                       |         | 4.0                   |                       | 40.0                  | μA    |

## **Noise Characteristics**

|                                 |   |                     | $T_A = 25^{\circ}C$ |        |       |               |
|---------------------------------|---|---------------------|---------------------|--------|-------|---------------|
| Symbol                          | Parameter                                       | V <sub>CC</sub> (V) | Тур.                | Limits | Units | Conditions    |
| V <sub>OLP</sub> <sup>(2)</sup> | Quiet Output Maximum<br>Dynamic V <sub>OL</sub> | 5.0                 | 0.4                 | 0.8    | V     | $C_L = 50 pF$ |
| V <sub>OLV</sub> <sup>(2)</sup> | Quiet Output Minimum<br>Dynamic V <sub>OL</sub> | 5.0                 | -0.4                | -0.8   | V     | $C_L = 50 pF$ |
| V <sub>IHD</sub> <sup>(2)</sup> | Minimum HIGH Level Dynamic<br>Input Voltage     | 5.0                 |                     | 3.5    | V     | $C_L = 50 pF$ |
| V <sub>ILD</sub> <sup>(2)</sup> | Maximum LOW Level Dynamic<br>Input Voltage      | 5.0                 |                     | 1.5    | V     | $C_L = 50 pF$ |

Note:

2. Parameter guaranteed by design.

## **AC Electrical Characteristics**

|                                     |  |                     |                        | т    | A = 25° | C    |      | –40°C<br>85°C |       |
|-------------------------------------|--|---------------------|------------------------|------|---------|------|------|---------------|-------|
| Symbol                              | Parameter  | V <sub>CC</sub> (V) | Conditions             | Min. | Тур.    | Max. | Min. | Max.          | Units |
| f <sub>MAX</sub>                    | Maximum Clock  | 3.3 ± 0.3           | $C_L = 15 pF$          | 90   | 140     |      | 75   |               | MHz   |
|                                     | Frequency  |                     | $C_L = 50 pF$          | 50   | 75      |      | 45   |               | 1     |
|                                     |  | 5.0 ± 0.5           | $C_L = 15 pF$          | 150  | 210     |      | 125  |               | MHz   |
|                                     |  |                     | $C_L = 50 pF$          | 85   | 115     |      | 75   |               | 1     |
| t <sub>PLH</sub> , t <sub>PHL</sub> | Propagation Delay Time,                                      | 3.3 ± 0.3           | $C_L = 15 pF$          |      | 7.5     | 11.5 | 1.0  | 13.5          | ns    |
|                                     | (CP to $Q_n$ or $\overline{Q}_n$ )                           |                     | $C_L = 50 pF$          |      | 10.0    | 15.0 | 1.0  | 17.0          |       |
|                                     |  | 5.0 ± 0.5           | $C_L = 15 pF$          |      | 4.8     | 7.3  | 1.0  | 8.5           | ns    |
|                                     |  |                     | $C_L = 50 pF$          |      | 6.3     | 9.3  | 1.0  | 10.5          |       |
| t <sub>PLH</sub> , t <sub>PHL</sub> | Propagation Delay Time,                                      | 3.3 ± 0.3           | $C_L = 15 pF$          |      | 6.3     | 10.1 | 1.0  | 12.0          | ns    |
|                                     | $(\overline{MR} \text{ to } Q_n \text{ or } \overline{Q}_n)$ |                     | $C_L = 50 pF$          |      | 8.8     | 13.6 | 1.0  | 15.5          |       |
|                                     |  | 5.0 ± 0.5           | $C_L = 15 pF$          |      | 4.3     | 6.4  | 1.0  | 7.5           | ns    |
|                                     |  |                     | $C_L = 50 pF$          |      | 5.8     | 8.4  | 1.0  | 9.5           |       |
| $t_{\rm OSLH},t_{\rm OSHL}$         | Output to Output Skew  | 3.3 ± 0.3           | $C_L = 50 pF$          |      |         | 1.5  |      | 1.5           |       |
|                                     |  | 5.0 ± 0.5           | $C_{L} = 50 p F^{(3)}$ |      |         | 1.0  |      | 1.0           |       |
| C <sub>IN</sub>                     | Input Capacitance  |                     | V <sub>CC</sub> = Open |      | 4       | 10   |      | 10            | pF    |
| C <sub>PD</sub>                     | Power Dissipation<br>Capacitance                             |                     | (4)                    |      | 44      |      |      |               | pF    |

#### Notes:

3. Parameter guaranteed by design.  $t_{OSLH} = |t_{PLHmax} - t_{PLHmin}|; t_{OSHL} = |t_{PHLmax} - t_{PHLmin}|.$ 

4. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained from the equation:
I<sub>CC</sub> (opr.) = C<sub>PD</sub> • V<sub>CC</sub> • f<sub>IN</sub> + I<sub>CC</sub>/4 (per F/F), and the total C<sub>PD</sub> when n pcs of the Flip-Flop operate can be calculated by the following equation: C<sub>PD</sub> (total) = 30 + 14 • n

### **AC Operating Requirements**

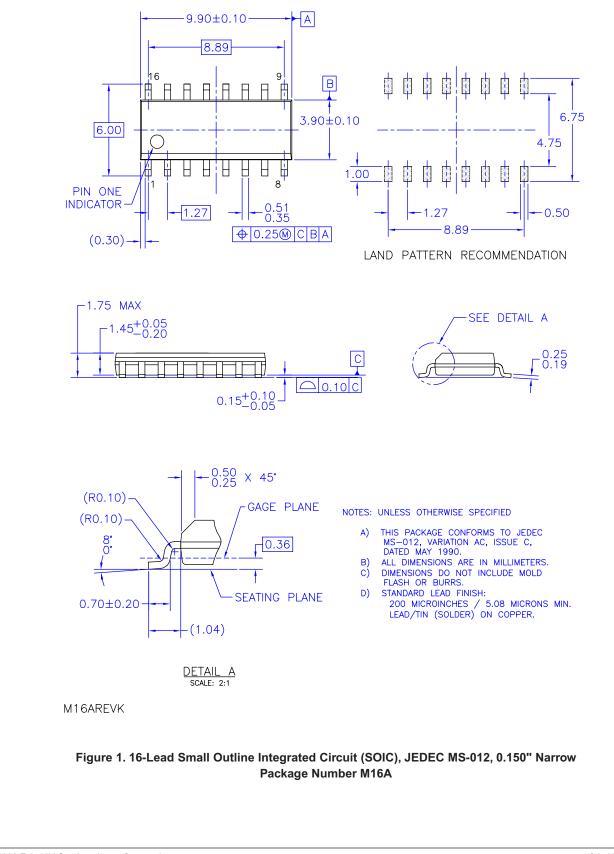
|  |                               |                                    | <b>T</b> <sub>A</sub> = | 25°C | $T_A = -40^{\circ}C \text{ to } +85^{\circ}C$ |       |
|--|-------------------------------|------------------------------------|-------------------------|------|---|-------|
| Symbol                                 | Parameter                     | V <sub>CC</sub> (V) <sup>(5)</sup> | Тур.                    | Gua  | ranteed Minimum                               | Units |
| t <sub>W</sub> (L), t <sub>W</sub> (H) | Minimum Pulse Width (CP)      | 3.3                                |                         | 5.0  | 5.0   | ns    |
|  |                               | 5.0                                |                         | 5.0  | 5.0   |       |
| t <sub>W</sub> (L)                     | Minimum Pulse Width (MR)      | 3.3                                |                         | 5.0  | 5.0   | ns    |
|  |                               | 5.0                                |                         | 5.0  | 5.0   |       |
| t <sub>S</sub>                         | Minimum Setup Time (Dn to CP) | 3.3                                |                         | 5.0  | 5.0   | ns    |
|  |                               | 5.0                                |                         | 4.0  | 4.0   |       |
| t <sub>H</sub>                         | Minimum Hold Time (Dn to CP)  | 3.3                                |                         | 1.0  | 1.0   | ns    |
|  |                               | 5.0                                |                         | 1.0  | 1.0   |       |
| t <sub>REC</sub>                       | Minimum Removal Time (MR)     | 3.3                                |                         | 5.0  | 5.0   | ns    |
|  |                               | 5.0                                |                         | 5.0  | 5.0   |       |

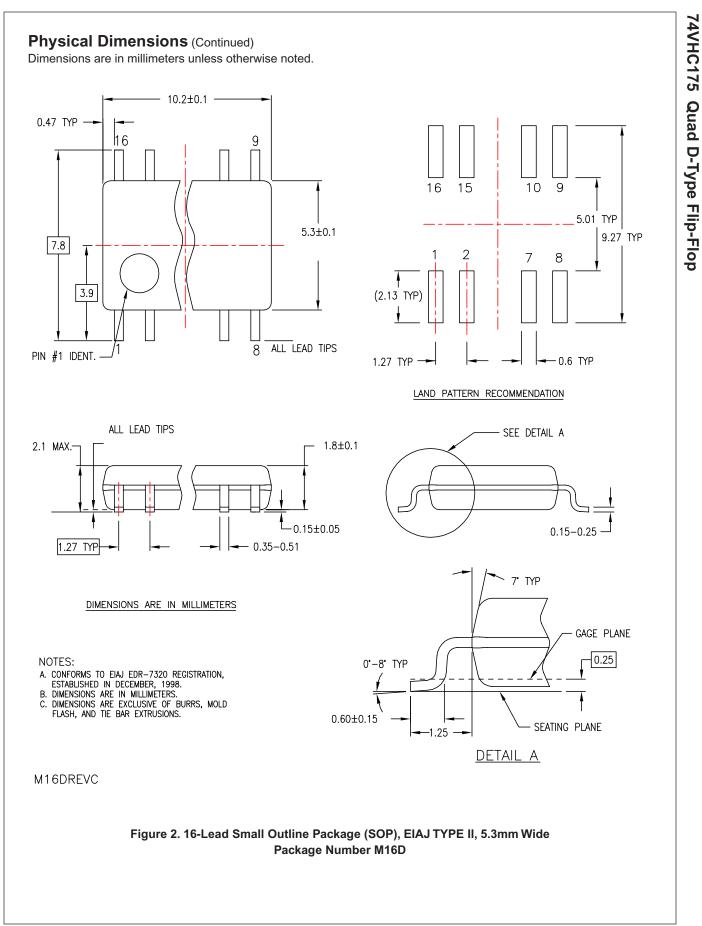
#### Note:

5.  $V_{CC}$  is 3.3  $\pm$  0.3V or 5.0  $\pm$  0.5V

#### **Physical Dimensions**

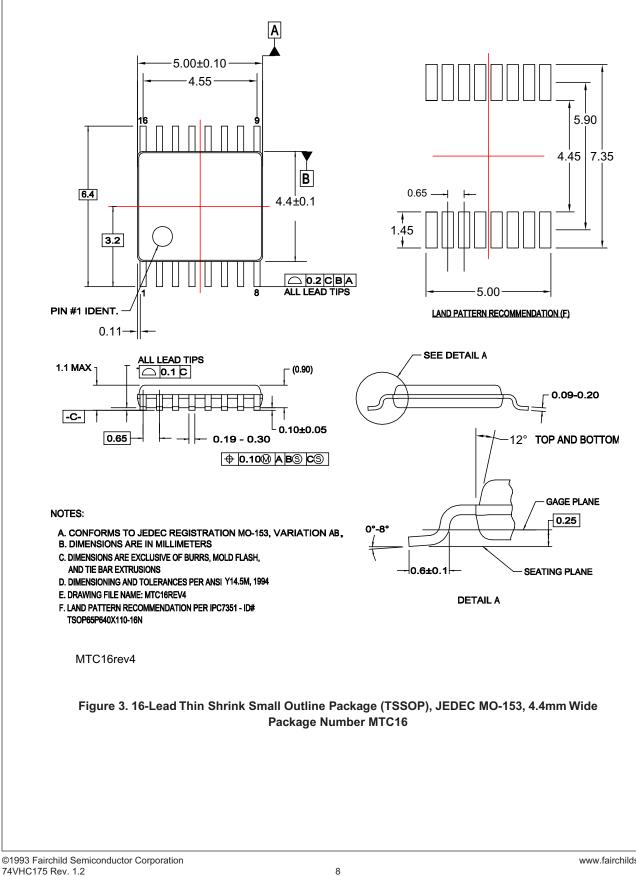
Dimensions are in millimeters unless otherwise noted.





#### Physical Dimensions (Continued)

Dimensions are in millimeters unless otherwise noted.





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