

82S90 PRESETTABLE HIGH SPEED DECADE/BINARY COUNTER

82S91 ADVANCED INFORMATION DIGITAL 8000 SERIES SCHOTTKY TTL/MSI

DESCRIPTION

The 82S90 Decade Counter and 82S91 Binary Counter are high speed devices providing a wide variety of counter/storage register applications with a minimum number of packages.

The 82S91 Binary Counter may be connected as a divide-by-two; four, eight, or sixteen counter.

Both devices have strobed parallel-entry capability so that the counter may be set to any desired output state. A "1" or "0" at a data input will be transferred to the associated output when the strobe input is put at the "0" level. For additional flexibility, both units are provided with a reset input which is common to all four bits. A "0" on the reset lines produces "0" at all four outputs.

FEATURES

- SCHOTTKY-CLAMPED TTL STRUCTURE
- PNP INPUTS
- CLOCK FREQUENCY = 100 MHz (Typ)
- PARALLEL LOAD CAPABILITY
- ASYNCHRONOUS RESET

ELECTRICAL CHARACTERISTICS

Clock Frequency	100 MHz (Typ)
Strobe/Reset Hold Time	10ns (Typ)
Output Current (Min)	
I _{out} "0"	20mA @ 0.5V
I _{out} "1"	1mA @ 2.7V

LOGIC DIAGRAMS AND TRUTH TABLES

