

MEMORY ADDRESS DIRECTOR

8X360

FEATURES

- Address control for working storage
- 16-bit addressing capability
- Byte and word addressing support
- Automatic increment and decrement
- 11 Address and word-count registers
- Reduces number of 8X305 instructions required

PRODUCT DESCRIPTION

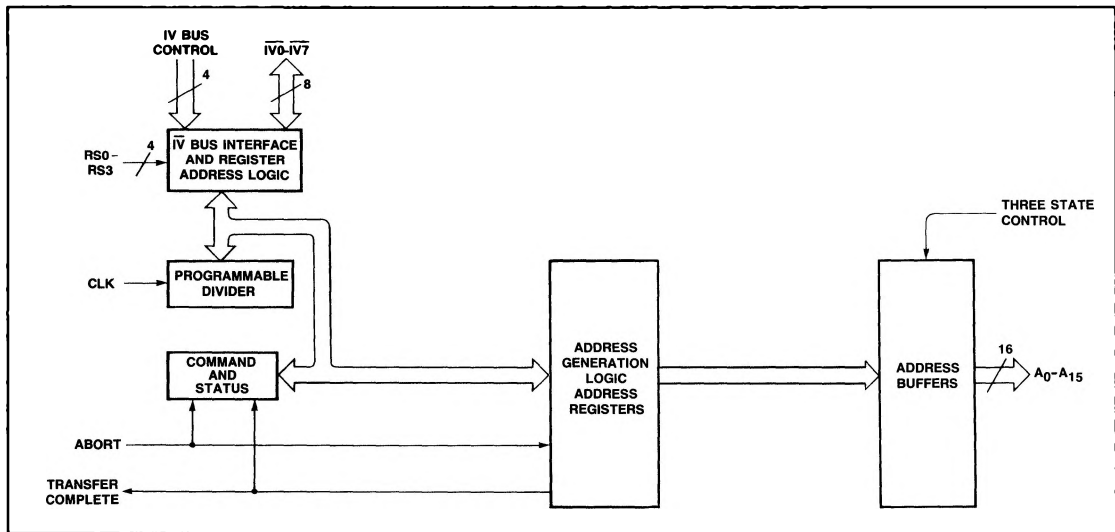
The 8X360 Memory Address Director (MAD) is a high-performance member of the 8X300 Family that generates sequential memory addresses to facilitate the transfer of data to and from memory. The MAD provides a highly-efficient and cost-effective

solution for DMA and other applications requiring large working-storage memories and high-speed data transfers. Once initialized with such information as starting address, ending address, byte count, address increment, address decrement, etc., the 8X360 performs all bookkeeping chores automatically and all address-management software is off-loaded from the processor. The 8X360 can be addressed by conventional means or by extended microcode; system status is available to the user via I/O pins.

ORDERING INFORMATION

N8X360N, N8X360I, S8X360I

BLOCK DIAGRAM OF 8X360



8X360 APPLICATIONS

