Second Mixer IF for digital cellular phones BH4128FV

The BH4128FV is an IC equipped with internal second mixer and IF amplifier circuits, developed for use with digital cellular phones.

Applications

Digital cellular phones

Features

- 1) Equipped with internal mixer, IF, and RSSI circuits.
- Can be operated at mixer input frequencies ranging from 10MHz to 200MHz.
- 3) IF amplifier gain is 100 dB.
- 4) Equipped with a battery power saving function.

● Absolute maximum ratings (Ta = 25 °C, for measurement circuit)

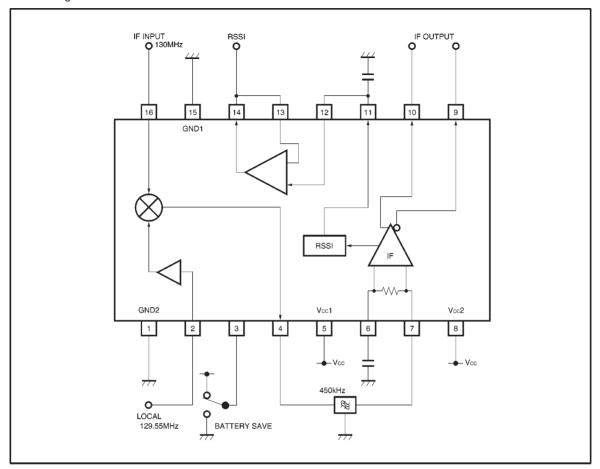
Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	7.0	V
Power dissipation	Pd	350*	mW
Storage temperature	Tstg	−55∼+125	°C

^{*} Reduced by 3.5mW for each increase in Ta of 1°C over 25°C.

• Recommended operating conditions (Ta = 25 °C, for measurement circuit)

Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	2.3~5.5	V
Operating temperature	Tpor	−40~+85	°C

Block diagram



Pin descriptions

Pin No.	Function	Internal peripheral circuit	DC voltage (V)
1	GND pin 2	GND for MIX stage	GND
2	Local oscillation input pin Input from external oscillator	2 to MIXER 2.4k BIAS	_
3	Battery save pin "Pin 3 voltage" ≤0.2V: Battery save 2V≦ "Pin 3 voltage" ≤Vcc: Active	3 50k	-
4	Mixer output pin Connect ceramic filter Output impedance: 1 kΩ	900 4	Vcc-1.6
5	Vcc pin 1	Vcc for MIX stage and IF amplifier early stage	Vcc

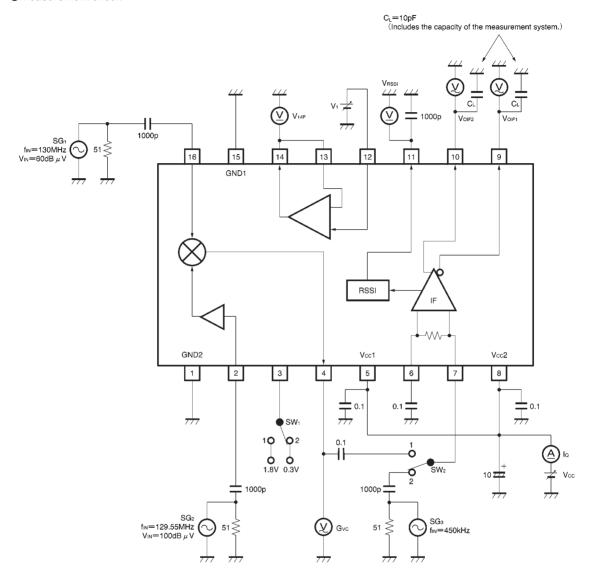
Pin No.	Function	Internal peripheral circuit	DC voltage (V)
6	IF amplifier bypass pin Connect capacitor	V∞ → 15k ≥ 500 →	Vcc-0.6
7	IF amplifier input pin Connect ceramic filter Output impedance: 1 $k\Omega$	7 8 8	Vcc-0.6
8	Vcc pin 2	Vcc for IF later stage	Vcc
9, 10	IF amplifier output pin Pins 9 and 10 are opposite-phase output	9 or 10	Vcc-1.2
11	RSSI output pin Connect capacitor	Vcc Vcc 111 50k ≥	0.15

Pin No.	Function	Internal peripheral circuit	DC voltage (V)
12	Buffer amplifier non-inverting input pin	Voc	
13	Buffer amplifier inverting input pin	12 13	_
14	Buffer amplifier output pin	200 W/77	_
15	GND pin 1	GND for IF stage and RSSI circuit	GND
16	Mixer input pin Connect first IF signal from DC cutoff	Voc 1.6k	1.2

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Quiescent current	lα	_	3.0	3.9	mA	V _{IN (LO)} =100dB μV
Battery save quiescent current	IQ (BS)	_	0	5	μΑ	
Datter, and function involved	Vтн-н	2	_	Vcc	V	Active
Battery save function input voltage	V _{TH} -L	GND	_	0.2	V	Battery save
〈MIX - oscillator section〉						
Mixer operating frequency	fмıx	10	130	200	MHz	
Mixer conversion gain	Gvc	8.5	12.5	16.5	dB	$V_{IN (MIX)} = 60 dB \mu V, RL = 1 k\Omega$
-1 dB compression output level	Vом	_	101	_	dB μ V	
3rd order intercept point	IРз	_	115	_	dB μ V	f1=130.05MHz, f2=130.10MHz
Noise figure	NF	_	8.5	_	dB	LC matching input
Mixer input admittance	YIN (MIX)	_	0.38+j2.75	_	ms	f=130MHz
Mixer output resistance	Ro (MIX)	_	1	_	kΩ	
Local input level	VIN (LO)	95	100	105	dB μV	
Local input admittance	YIN (LO)	_	0.25+j3.65	_	ms	f=130MHz
⟨IF section⟩		•				
IF operating frequency	fıF	350	_	500	kHz	
IF amplifier gain	Gv	_	100	_	dB	
IF input resistance	RIN (IF)	_	1	_	kΩ	
IF output level	Voif	0.7	1	1.3	V _{P-P}	V _{IN (IF)} =80dB μV
IF output duty ratio	DR	45	50	55	%	V _{IN (IF)} =80dB μV, C _L =10pF
〈RSSI section〉		•				
Output voltage 1	VRSSI1	_	0.15	0.4	V	No input
Output voltage 2	VRSSI2	1.0	1.2	1.4	V	V _{IN (IF)} =65dB μV
Output voltage 3	VRSSI3	1.9	2.0	2.2	V	V _{IN (IF)} =100dB μ V
Dynamic range	DR	_	85	-	dB	
Output resistance	Ro (RSSI)	40	50	60	kΩ	
Rise time at power on	Тон	_	140	-	μs	CL=1000pF, VIN (MIX)=60dB μ V
Fall time at power off	Toff	_	130	_	μs	CL=1000pF, VIN (MIX)=60dB μ V
RSSI rise time	TR	_	270	_	μS	CL=1000pF, VIN (MIX)=60dB μ V
RSSI fall time	TF	_	220	_	μS	CL=1000pF, VIN (MIX)=60dB μ V

ONot designed for radiation resistance.

Measurement circuit



Units : Resistance : Ω Capacitance : μ F

Fig. 1

Application example

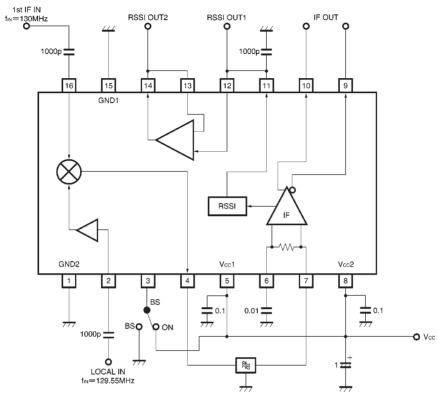


Fig. 2

Units : Resistance : Ω Capacitance : μ F

External dimensions (Units: mm)

