

DM7090/DM8090 quad inverter/2-input NAND buffer

general description

This monolithic TTL device contains four inverters and two 2-input NAND gates. It was designed to reduce package counts in applications where a gate is needed occasionally in the same card location as inverters.

The two NAND gates may also be used as inverters, by making the two inputs common. DM7090/DM8090 characteristics are similar to those of other gates and inverters in the 54/74 series and

are completely compatible with 54/74 devices. Other features are:

- Input clamp diodes
- Fanout of 10
- Typical propagation delay of 13 ns
- Typical power dissipation of 10 mW/gate or inverter
- Noise immunity is 1V typical, 400 mV guaranteed.

connection diagram

