LA5315M



# Variable Divided Voltage Generator for LCD Use

## Overview

The LA5315M is a variable divided voltage generator IC for multiple drive of LCD matrix.

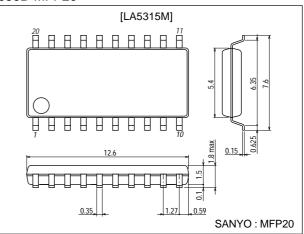
### Features

- Power supply for variable bias LCD drive (1/5 to 1/13 bias available by internal resistances).
- 5 voltage outputs.
- Low current drain (1.5mA max).
- Miniflat package.

## **Package Dimensions**

unit:mm

3036B-MFP20



# **Specifications**

### Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max	GND-V <sub>CC</sub>	–35 to 0	V
Maximum output current	IOUT max	V <sub>1</sub> , V <sub>2</sub> , V <sub>3</sub> , V <sub>4</sub> , V <sub>5</sub>	15	mA
Allowable power dissipation	Pd max		370	mW
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-30 to +125	°C

#### **Operating Conditions** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	VCC	GND-V <sub>CC</sub> : (When V <sub>1</sub> >–1V, I <sub>IN</sub> is needed.) *	-30 to -10	V
Recommended input voltage	V <sub>REF</sub>	GND-V <sub>REF</sub> : V <sub>REF</sub> ≥ V <sub>CC</sub> *	-30 to -6	V
Recommended input current	I <sub>IN</sub>	$V_{IN}$ : $V_1{>}{-}1V,$ current source of $I_{IN}$ : 1V or greater relative to GND	0.2 to 3	mA
	IOUT1	V <sub>1</sub>	-0.1 to +5	mA
Recommended output current	I <sub>OUT2</sub> , 3	V <sub>2</sub> , V <sub>3</sub>	–5 to +5	mA
	I <sub>OUT4</sub> , 5	V <sub>4</sub> , V <sub>5</sub>	-10 to +0.1	mA

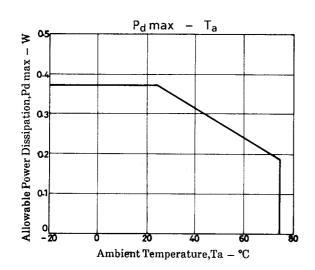
note \* Set  $V_{CC}, V_{REF}$  so that  $\mid V_2 \mid, \mid V_{CC}\text{-}V_5 \mid$  become 1V or greater.

- Any and all SANYO products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your SANYO representative nearest you before using any SANYO products described or contained herein in such applications.
- SANYO assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO products described or contained herein.

SANYO Electric Co., Ltd. Semiconductor Company TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

# Operating Characteristics at Ta = $25^{\circ}$ C, V<sub>CC</sub>=-16V

Parameter	Symbol	Conditions		Ratings		
Falameter	Symbol	Conditions	min	typ	max	Unit
Current drain	ICC	$V_{IN}$ , GND- $V_{CC}$ , $V_{REF}$ : $V_{CC}$ = $V_{REF}$ =-16V, $V_{IN}$ =GND, $R_X$ =5R			1.5	mA
Output voltage ratio 1	Ra1	$V_2/V_1$		2.00	2.04	
Output voltage ratio 2	Ra2	$(V_5 - V_3) / (V_5 - V_4)$ Vref=–12V, V_CC=–16V, 1/9 bias (R_{\chi}=5R)		2.00	2.04	
Output voltage ratio 3	Rb1	V <sub>5</sub> /V <sub>1</sub> Vref=-12V, V <sub>CC</sub> =-16V, 1/9 bias (R <sub>X</sub> =5R)	8.73	9.00	9.27	
Output voltage ratio 4	Rb2	$V_5/V_2$ Vref=-12V, $V_{CC}$ =-16V, 1/9 bias (R <sub>X</sub> =5R)	4.37	4.50	4.63	
Output voltage ratio 5	Rb3	$V_{5}/(V_{5}\text{-}V_{3})$ Vref=–12V, $V_{CC}$ =–16V, 1/9 bias (R_{\chi}=5R)	4.37	4.50	4.63	
Output voltage ratio 6	Rb4	$V_{5}/(V_{5}\text{-}V_{4})$ Vref=–12V, $V_{CC}$ =–16V, 1/9 bias (R_{\chi}=5R)	8.73	9.00	9.27	
Internal resistance ratio 1	4R	$V_{\text{IN}}3\text{-}R_{X}1$ Resistance ratio referenced to R across pins (5) and (6)		4		
Internal resistance ratio 2	5R	$V_{IN}3\mbox{-}R_X2$ Resistance ratio referenced to R across pins (5) and (6)		5		
Internal resistance ratio 3	6R	$V_{IN}3\mbox{-}R_X3$ Resistance ratio referenced to R across pins (5) and (6)		6		
Internal resistance ratio 4	7R	$V_{IN}3\mbox{-}R_X4$ Resistance ratio referenced to R across pins (5) and (6)		7		
Internal resistance ratio 5	8R	$V_{\text{IN}}3\text{-}R_{\text{X}}5$ Resistance ratio referenced to R across pins (5) and (6)		8		
Internal resistance ratio 6	9R	$V_{\text{IN}}3\text{-}R_{X}6$ Resistance ratio referenced to R across pins (5) and (6)		9		
Resistance	R	$$R_{X}1$-}R_{X}2$ : R value when 0.5V is applied across pins (5) and (6)		20		kΩ
Load regulation 1	ΔV <sub>1</sub>	V <sub>1</sub> :+100µA <i<sub>OUT1&lt;+5mA</i<sub>			20	mV
Load regulation 2	ΔV2	V <sub>2</sub> :+100µA <i<sub>OUT2&lt;+5mA</i<sub>			20	mV
Load regulation 3	ΔV <sub>3</sub>	V3:+100µA <iout3<+5ma< td=""><td></td><td></td><td>20</td><td>mV</td></iout3<+5ma<>			20	mV
Load regulation 4	-ΔV2	V <sub>2</sub> : -5mA <i<sub>OUT2&lt;-100µA</i<sub>			20	mV
Load regulation 5	$-\Delta V_3$	V <sub>3</sub> : -5mA <i<sub>OUT3&lt;-100µA</i<sub>		20	mV	
Load regulation 6	$-\Delta V_4$	V <sub>4</sub> :-10mA <i<sub>OUT4&lt;-100µA</i<sub>		20	mV	
Load regulation 7	$-\Delta V_5$	V5:-10mA <iout5<-100µa< td=""><td>20</td><td>mV</td></iout5<-100µa<>		20	mV	

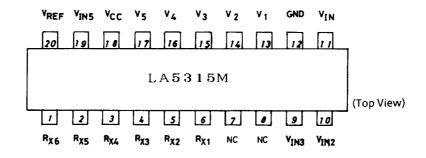


### **Pin Functions**

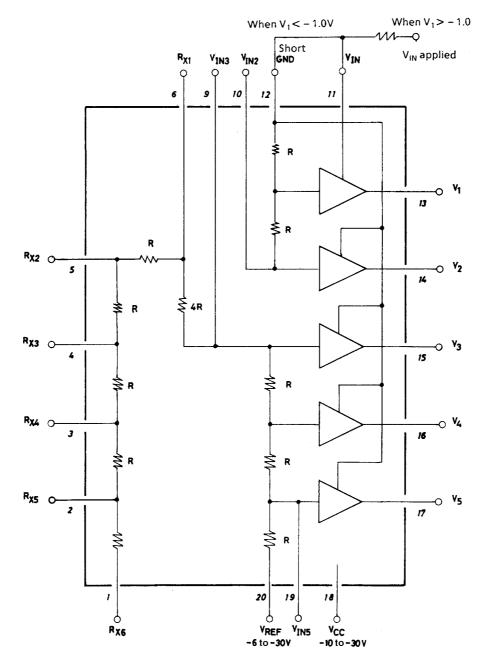
Pin No.	Pin Name	Description	Remarks
1	R <sub>X6</sub>	R <sub>X</sub> pin	Pin 10 shorted R <sub>X</sub> =9R
2	R <sub>X5</sub>	R <sub>X</sub> pin	Pin 10 shorted R <sub>X</sub> =8R
3	R <sub>X4</sub>	R <sub>X</sub> pin	Pin <sup>(1)</sup> shorted R <sub>X</sub> =7R
4	R <sub>X3</sub>	R <sub>X</sub> pin	Pin 10 shorted R <sub>X</sub> =6R
5	R <sub>X2</sub>	R <sub>X</sub> pin	Pin 10 shorted R <sub>X</sub> =5R
6	R <sub>X</sub> 1	$R_{\chi}$ pin	Pin 10 shorted R <sub>X</sub> =4R
7		NC	
8		NC	
9	V <sub>IN</sub> 3	V <sub>3</sub> input	
10	V <sub>IN</sub> 2	V <sub>2</sub> input	
11	VIN	V <sub>1</sub> supply (+ supply)	When $V_1 > -1.0V$ , $V_{IN}$ is applied.
	V IN		When $V_1 < -1.0V$ , this pin is shorted to GND.
12	GND	GND	
13	V <sub>1</sub>	V <sub>1</sub> output	
14	V <sub>2</sub>	V <sub>2</sub> output	
15	V <sub>3</sub>	V <sub>3</sub> output	
16	V <sub>4</sub>	V <sub>4</sub> output	
17	V5	V <sub>5</sub> output	
18	V <sub>CC</sub>	V <sub>CC</sub> supply (–supply)	
19	V <sub>IN5</sub>	V <sub>5</sub> input	
20	VREF	VREF supply (-supply)	

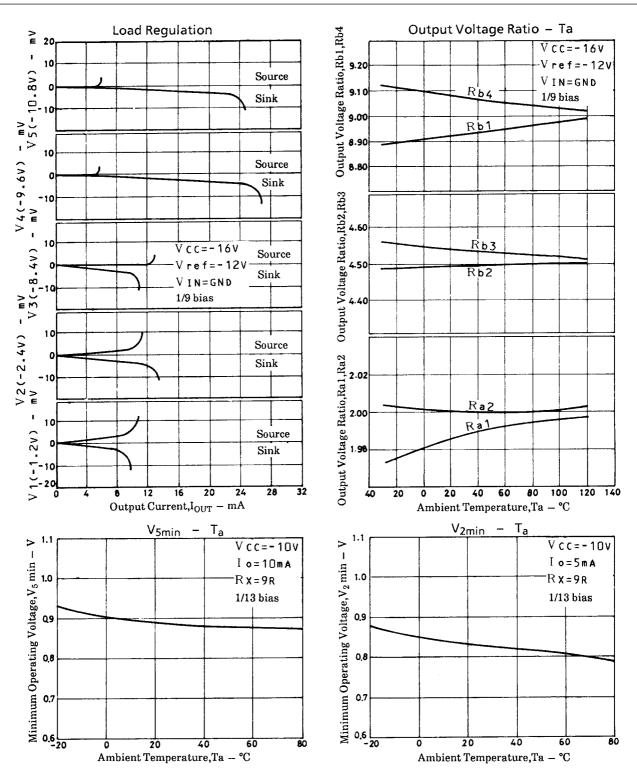
Note ) Do not use the NC pin.

### **Pin Assingment**



# Equivalent Circuit Block Diagram





- Specifications of any and all SANYO products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- SANYO Electric Co., Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of SANYO Electric Co., Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of March, 2000. Specifications and information herein are subject to change without notice.