

Variable Divided Voltage Generator for LCD

Overview

The LA5316M is a variable divided voltage generator IC for multiple drive of LCD matrix.

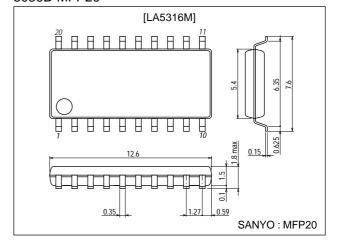
Features

- Power supply for variable bias LCD drive (1/5 to 1/13 bias available by on-chip resistances).
- 5 operational amplifiers to deliver 5 voltage outputs.
- Low current drain (1.5mA max).
- Miniflat package.
- On-chip variable voltage regulator for V_{REF}.

Package Dimensions

unit:mm

3036B-MFP20



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max	GND-V _{CC}	-35 to 0	V
Maximum output current	I _{OUT} max	V1, V2, V3, V4, V5	15	mA
Allowable power dissipation	Pd max		370	mW
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-30 to +125	°C

Operating Conditions at $Ta = 25^{\circ}C$

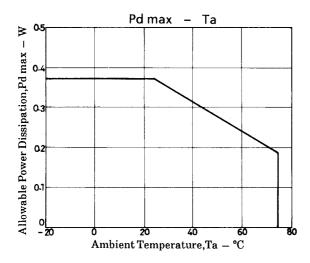
Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V _{CC} op	GND-V _{CC} : (When V1>-1V, I _{IN} is needed.) *	−30 to −10	V
Recommended input voltage	V _{REF}	GND-V _{REF} : V _{REF} ≥ V _{CC} *	−30 to −6	V
Recommended input current	I _{IN}	V _{IN} : V1>–1V, current source of I _{IN} : 1V or greater relative to GND	0.2 to 3	mA
	I _{OUT} 1	V1	-0.1 to +5	mA
Recommended output current	I _{OUT} 2, 3	V2, V3	−5 to +5	mA
	I _{OUT} 4, 5	V4, V5	-10 to +0.1	mA

note * Set V_{CC}, V_{REF} so that $\mid V2 \mid, \mid V_{CC}\text{-}V5 \mid$ become 1V or greater.

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Operating Characteristics at $Ta=25^{\circ}C,\,V_{CC}\!\!=\!\!-16V$

Parameter	Symbol	Conditions		Ratings		Unit
			min	typ	max	
Current drain	Icc	V _{IN} , GND-V _{CC} , V _{REF} : V _{CC} =V _{REF} =-16V, V _{IN} =GND, R _X =5R			1.5	mA
Output voltage ratio 1	Ra1	V2/V1, Vref=–12V, V _{CC} =–16V, 1/9 bias (R _X =5R)		2.00	2.04	
Output voltage ratio 2	Ra2	(V5-V3)/(V5-V4), Vref=-12V, V _{CC} =-16V, 1/9 bias (R _X =5R)		2.00	2.04	
Output voltage ratio 3	Rb1	V5/V1, Vref=–12V, V _{CC} =–16V, 1/9 bias (R _X =5R)		9.00	9.27	
Output voltage ratio 4	Rb2	V5/V2, Vref=-12V, V _{CC} =-16V, 1/9 bias (R _X =5R)	4.37	4.50	4.63	
Output voltage ratio 5	Rb3	V5/(V5-V3), Vref=–12V, V _{CC} =–16V, 1/9 bias (R _X =5R)		4.50	4.63	
Output voltage ratio 6	Rb4	V5/(V5-V4), Vref=–12V, V _{CC} =–16V, 1/9 bias (R _X =5R)		9.00	9.27	
Internal resistance ratio 1	4R	V _{IN} 3-R _X 1, Resistance ratio referenced to R across pins ⑤ and ⑥		4		
Internal resistance ratio 2	5R	V _{IN} 3-R _X 2, Resistance ratio referenced to R across pins ⑤ and ⑥		5		
Internal resistance ratio 3	6R	V _{IN} 3-R _X 3, Resistance ratio referenced to R across pins ⑤ and ⑥		6		
Internal resistance ratio 4	7R	V _{IN} 3-R _X 4, Resistance ratio referenced to R across pins ⑤ and ⑥		7		
Internal resistance ratio 5	8R	V _{IN} 3-R _x 5, Resistance ratio referenced to R across pins (§) and (§)		8		
Internal resistance ratio 6	9R	V _{IN} 3-R _x 6, Resistance ratio referenced to R across pins ⑤ and ⑥		9		
Resistance	R	R _X 1-R _X 2 : R value when 0.5V is applied across pins ⑤ and ⑥		20		kΩ
Load regulation 1	ΔV1	V1: +100μA <i<sub>OUT1<+5mA</i<sub>			20	mV
Load regulation 2	ΔV2	V2 : +100μA <i<sub>OUT2<+5mA</i<sub>			20	mV
Load regulation 3	ΔV3	V3 : +100μA <i<sub>OUT3<+5mA</i<sub>			20	mV
Load regulation 4	–∆V2	V2 : -5mA <i<sub>OUT2<-100μA</i<sub>			20	mV
Load regulation 5	-∆V3	V3 : -5mA <i<sub>OUT3<-100μA</i<sub>			20	mV
Load regulation 6	–∆V4	V4 : -10mA <i<sub>OUT4<-100μA</i<sub>		20	mV	
Load regulation 7	- ΔV5			20	mV	
Regulator voltage	V _{Reg}			-5.9	V	
VReg load regulation	ΔV _{Reg}			50	mV	

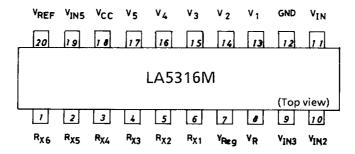


Pin Functions

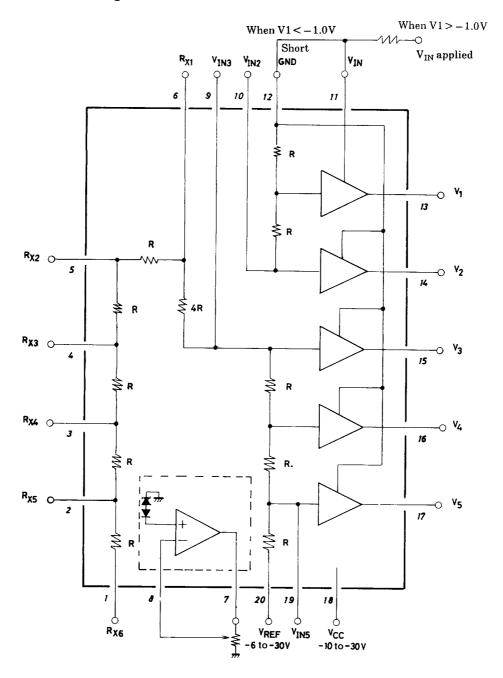
Pin No.	Pin Name	Description	Remarks
1	R _{X6}	R _X pin	Pin ® shorted R _X =9R
2	R _{X5}	R _X pin	Pin ® shorted R _X =8R
3	R _{X4}	R _X pin	Pin ® shorted R _X =7R
4	R _{X3}	R _X pin	Pin ® shorted R _X =6R
5	R _{X2}	R _X pin	Pin ① shorted R _X =5R
6	R _X 1	R _X pin	Pin ® shorted R _X =4R
7	V _{Reg}	V _{Reg} output	For supplying V _{REF}
8	V _R	V _{Reg} operational amplifier	
9	V _{IN} 3	V ₃ input	
10	V _{IN} 2	V ₂ input	
11	VIN	V ₁ supply (+ supply)	When $V_1 > -1.0V$, V_{IN} is applied. When $V_1 < -1.0V$, this pin is shorted to GND.
12	GND	GND	
13	V ₁	V ₁ output	
14	V ₂	V ₂ output	
15	V ₃	V ₃ output	
16	٧4	V ₄ output	
17	V ₅	V ₅ output	
18	VCC	V _{CC} supply (-supply)	
19	V _{IN5}	V ₅ input	
20	V _{REF}	V _{REF} supply (-supply)	

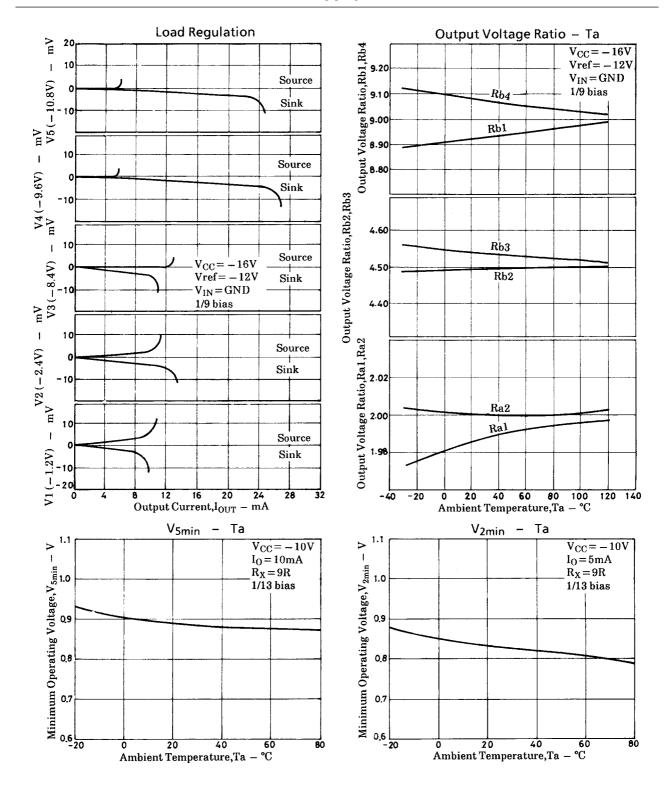
Note) Do not use the NC pin.

Pin Assingment



Equivalent Circuit Block Diagram





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