CMOS LSI



Quad Bilateral Switch

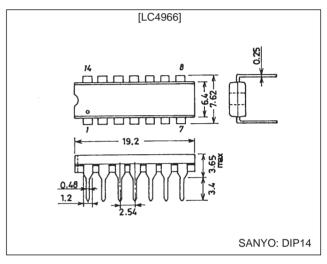
Overview

The LC4966 is an IC that provides the same functions as the MLC4066B and the MLC4066BH over an expanded usable voltage range. The LC4966 provides four bidirectional switch circuits. These circuits form a lowimpedance conducting path between the input and output sides when the corresponding control input (CONT) is set high, and form a high-impedance nonconducting open circuit when the control input is set low.

Package Dimensions

unit: mm

3003A-DIP14



Specifications

Absolute Maximum Ratings at Ta = 25°C, V_{SS} = 0 V

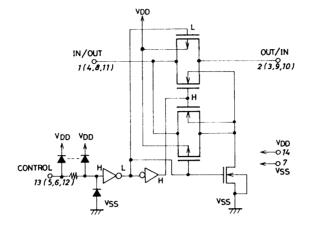
Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{DD} max		V_{SS} – 0.5 to V_{SS} + 40	V
Input voltage	V _{IN}		$V_{SS}{-}0.5$ to V_{DD} + 0.5	V
Output voltage	V _{OUT}		$V_{SS}{-}0.5$ to V_{DD} + 0.5	V
Input current	I _{IN}		±10	mA
Potential difference between input and output when on	V _I -V _O		±0.5	V
Lead soldering temperature time	T _{sol}	t = 10 s	260	°C
Allowable power dissipation	Pd max	$Ta \le 85^{\circ}C$, $I_{IN} = \pm 10 \text{ mA}$	300	mW
Operating temperature	Topr		-40 to +85	°C
Storage temperature	Tstg		-65 to +150	°C

Allowable Operating Ranges at Ta = -40 to $+85^{\circ}C$

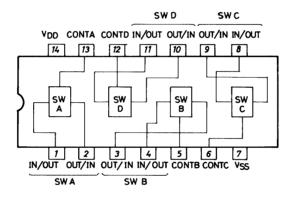
Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V _{DD}		3 to 37	V
Input voltage	V _{IN}		0 to V _{DD}	V

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Equivalent Circuit (1/4 LC4966)



Pin Assignment and Equivalent Circuit Block Diagram



Electrical Characteristics at Ta = 25 \pm 2°C, V_{SS} = 0 V

Parameter	Symbol	Conditions	min	typ	max	Unit
Input high-level control voltage	ViH	$V_{DD} = 5 \text{ V},$ for a current between input and output > 10 μ A	3.5			V
		V_{DD} = 10 V, for a current between input and output > 10 μ A	8.0			V
		V_{DD} = 15 V, for a current between input and output > 10 μ A	12.5			V
		$V_{DD} = 20 \text{ V},$ for a current between input and output > 10 μ A	17.0			V
		V_{DD} = 30 V, for a current between input and output > 10 µA	27.0			V
		V_{DD} = 37 V, for a current between input and output > 10 μ A	34.0			V
Input low-level control voltage	VIL	$V_{DD} = 5 V$, for a current between input and output < 10 μ A			1.0	V
		V_{DD} = 10 V, for a current between input and output < 10 μ A			2.0	V
		V_{DD} = 15 V, for a current between input and output < 10 μ A			2.5	V
		V_{DD} = 20 V, for a current between input and output < 10 μ A			2.5	V
		V_{DD} = 30 V, for a current between input and output < 10 μ A			3.0	V
		V_{DD} = 37 V, for a current between input and output < 10 μ A			3.0	V

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Parameter	Symbol	Conditions	min	typ	max	Unit
		V _{DD} = 5 V, V _{IN} = 5 V, I = ±1 mA		110	220	Ω
		V _{DD} = 5 V, V _{IN} = 2.5 V, I = ±1 mA		90	180	Ω
		V _{DD} = 5 V, V _{IN} = 0.25 V, I = ±1 mA		110	220	Ω
		V _{DD} = 10 V, V _{IN} = 10 V, I = ±3 mA		70	140	Ω
		V _{DD} = 10 V, V _{IN} = 5 V, I = ±3 mA		50	100	Ω
		V _{DD} = 10 V, V _{IN} = 0.25 V, I = ±3 mA		70	140	Ω
		V _{DD} = 15 V, V _{IN} = 15 V, I = ±3 mA		60	120	Ω
		V _{DD} = 15 V, V _{IN} = 7.5 V, I = ±3 mA		40	80	Ω
		V _{DD} = 15 V, V _{IN} = 0.25 V, I = ±3 mA		60	120	Ω
		V _{DD} = 20 V, V _{IN} = 20 V, I = ±3 mA		60	120	Ω
		V _{DD} = 20 V, V _{IN} = 10 V, I = ±3 mA		40	80	Ω
		$V_{DD} = 20 \text{ V}, \text{ V}_{IN} = 0.25 \text{ V}, \text{ I} = \pm 3 \text{ mA}$		60	120	Ω
		V _{DD} = 30 V, V _{IN} = 30 V, I = ±3 mA		50	100	Ω
		$V_{DD} = 30 \text{ V}, \text{ V}_{IN} = 15 \text{ V}, \text{ I} = \pm 3 \text{ mA}$		35	70	Ω
		$V_{DD} = 30 \text{ V}, \text{ V}_{IN} = 0.25 \text{ V}, \text{ I} = \pm 3 \text{ mA}$		50	100	Ω
		$V_{DD} = 37 \text{ V}, \text{ V}_{IN} = 37 \text{ V}, \text{ I} = \pm 3 \text{ mA}$		45	90	Ω
On resistance	R _{ON}	V _{DD} = 37 V, V _{IN} = 19 V, I = ±3 mA		30	60	Ω
		$V_{DD} = 37 \text{ V}, \text{ V}_{IN} = 0.25 \text{ V}, \text{ I} = \pm 3 \text{ mA}$		45	90	Ω
		V_{DD} = +5 V, V_{IN} = -5 V, V_{IN} = 5 V, I = ±3 mA		70	140	Ω
		V_{DD} = +5 V, V_{SS} = -5 V, V_{IN} = ±0.25 V, I = ±3 mA		50	100	Ω
		V_{DD} = +5 V, V_{SS} = -5 V, V_{IN} = -5 V, I = ±3 mA		70	140	Ω
		V_{DD} = +7.5 V, V_{SS} = -7.5 V, V_{IN} = 5 V, I = ±3 mA		60	120	Ω
		V_{DD} = +7.5 V, V_{SS} = -7.5 V, V_{IN} = ±0.25 V, I = ±3 mA		40	80	Ω
		V_{DD} = +7.5 V, V_{SS} = -7.5 V, V_{IN} = -5 V, I = ±3 mA		60	120	Ω
		V_{DD} = +10 V, V_{SS} = -10 V, V_{IN} = 10 V, I = ±3 mA		60	120	Ω
		V_{DD} = +10 V, V_{SS} = -10 V, V_{IN} = ±0.25 V, I = ±3 mA		40	80	Ω
		V_{DD} = +10 V, V_{SS} = -10 V, V_{IN} = -10 V, I = ±3 mA		60	120	Ω
		V_{DD} = +15 V, V_{SS} = -15 V, V_{IN} = 15 V, I = ±3 mA		50	100	Ω
		V_{DD} = +15 V, V_{SS} = -15 V, V_{IN} = ±0.25 V, I = ±3 mA		35	70	Ω
		V_{DD} = +15 V, V_{SS} = -15 V, V_{IN} = -15 V, I = ±3 mA		50	100	Ω
		V_{DD} = +18.5 V, V_{SS} = -18.5 V, V_{IN} = 18.5 V, I = ±3 mA		45	90	Ω
		V_{DD} = +18.5 V, V_{SS} = -18.5 V, V_{IN} = ±0.25 V, I = ±3 mA		30	60	Ω
		V_{DD} = +18.5 V, V_{SS} = -18.5 V, V_{IN} = -18.5 V, I = ±3 mA		45	90	Ω
Input off leakage current	I _{OFF}	V _{DD} = 37 V, V _{IN} = 37 V, V _{OUT} = 0 V		±1	±500	nA
1	UFF	V _{DD} = 37 V, V _{IN} = 0 V, V _{OUT} = 37 V		±1	±500	nA
Quiescent current drain		V _{DD} = 5 V		0.001	1	μA
		V _{DD} = 10 V		0.001	2	μA
	I _{DD}	V _{DD} = 15 V		0.002	4	μA
		V _{DD} = 20 V		0.004	8	μA
		$V_{DD} = 25 V$		0.01	20	μA
		$V_{DD} = 30 \text{ V}$		0.02	40	μA
		V _{DD} = 35 V		0.04	80	μA
		$V_{DD} = 37 V$		0.1	160	μA
Input high-level control current	I _{IH}	$V_{DD} = 37 \text{ V}, \text{ V}_{IN} = 37 \text{ V}$		10-4	3	μA
Input low-level control current	Ι _{ΙL}	$V_{\text{DD}} = 37 \text{ V}, \text{ V}_{\text{IN}} = 0 \text{ V}$		-10-4	3	μA
Input capacitance	C _{IN}	Control inputs		5	7.5	pF
•	- IN	Switch input and outputs		10		pF

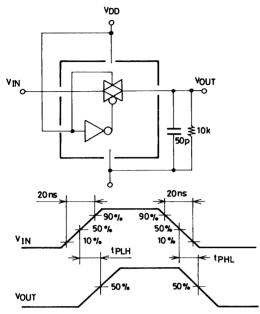
Switching Characteristics at $Ta=25\pm2^\circ C$

Parameter	Symbol	Conditions	min	typ	max	Unit
Transmission time (IN to OUT)	t _{PLH} , t _{PHL}	V_{DD} = 5 V, R _L = 10 k Ω , C _L = 50 pF		15	45	ns
		$V_{DD} = 10 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		10	30	ns
		$V_{DD} = 15 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		8	25	ns
		$V_{DD} = 20 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		8	25	ns
		$V_{DD} = 25 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		8	25	ns
		$V_{DD} = 30 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		7	25	ns
		$V_{DD} = 35 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		7	25	ns
		$V_{DD} = 37 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		7	25	ns
		V_{DD} = 5 V, R_L = 10 k Ω , C_L = 50 pF		100	200	ns
		$V_{DD} = 10 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		40	70	ns
		$V_{DD} = 15 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		35	60	ns
Transmission time		$V_{DD} = 20 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		35	60	ns
$(\text{control} \rightarrow \text{OUT})$	^t PLH ^{, t} PHL	$V_{DD} = 25 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		35	60	ns
		$V_{DD} = 30 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		35	60	ns
		$V_{DD} = 35 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		35	60	ns
		$V_{DD} = 37 \text{ V}, \text{ R}_{L} = 10 \text{ k}\Omega, \text{ C}_{L} = 50 \text{ pF}$		35	60	ns
	f max (c)	V _{DD} = 5 V, C _L = 15 pF		1.0		MHz
		V _{DD} = 10 V, C _L = 15 pF		1.0		MHz
Maximum control input frequency		V _{DD} = 20 V, C _L = 15 pF		1.0		MHz
		V _{DD} = 30 V, C _L = 15 pF		1.0		MHz
		V _{DD} = 37 V, C _L = 15 pF		1.0		MHz
	f max (I-O)	V_{DD} = +5 V, V_{SS} = -5 V, R_L = 10 k Ω , C_L = 15 pF ^{*1}		35		MHz
		V _{DD} = +10 V, V _{SS} = -10 V		40		MHz
Maximum transmission frequency		V _{DD} = +15 V, V _{SS} = -15 V		50		MHz
		V _{DD} = +18.5 V, V _{SS} = -18.5 V		50		MHz
Sine wave total harmonic distortion		V_{DD} = +5 V, V_{SS} = -5 V, R_L = 10 k Ω , f = 1 kHz ^{*2}		0.010		%
		V _{DD} = +10 V, V _{SS} = -10 V		0.005		%
		V _{DD} = +15 V, V _{SS} = -15 V		0.005		%
		V _{DD} = +18.5 V, V _{SS} = -18.5 V		0.005		%
Feedthrough (switch off state)		V_{DD} = +5 V, V_{SS} = -5 V, R_L = 10 k Ω^{*3}		1		MHz
		V _{DD} = +10 V, V _{SS} = -10 V		1		MHz
		V _{DD} = +15 V, V _{SS} = -15 V		1		MHz
		V _{DD} = +18.5 V, V _{SS} = -18.5 V		1		MHz

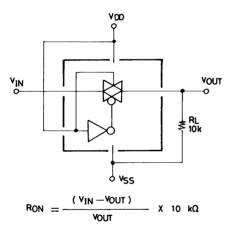
Vis is a ±2.5 Vp-p sine wave; fmax: 20log (Vos/Vis) = the -3 dB frequency.
Vis is a ±2.5 Vp-p sine wave.
Vis is a ±2.5 Vp-p sine wave. Frequency (feedthrough): 20log(Vos/Vis) = -50 dB

Test Circuits and Waveforms

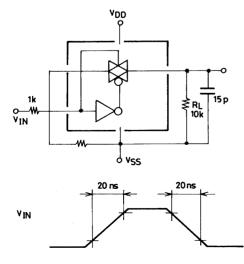
1. t_{PLH}, t_{PHL} (IN-OUT) Test Circuit



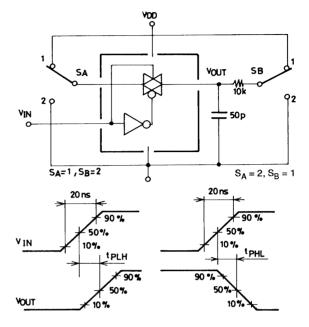
3. R_{ON} Test Circuit



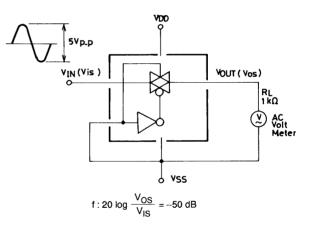
5. Crosstalk



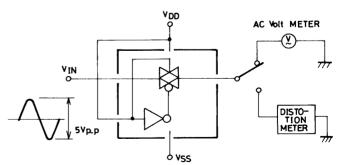
- Unit (resistance: Ω , capacitance: F)
- 2. t_{PLH}, t_{PHL} (Control-OUT) Test Circuit

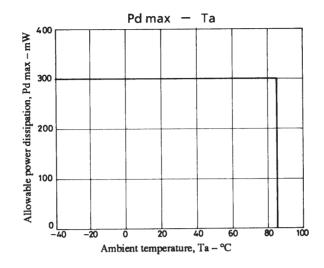


4. Feedthrough Test Circuit



6. Frequency Response (f max) and Total Harmonic Distortion





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