

## Enhanced Plastic 5A and 3A Low Dropout Fast Response Regulators

Check for Samples: [LMS1585AEP](#), [LMS1587EP](#)

### FEATURES

- Fast transient response
- Available in Adjustable, 1.5V, and 3.3V versions
- Current limiting and thermal protection
- Line regulation 0.005% (typical)

- Load regulation 0.05% (typical)

### APPLICATIONS

- Low voltage logic supplies
- Selected Military Applications
- Selected Avionics Applications

### DESCRIPTION

The LMS1585AEP and LMS1587EP are low dropout positive regulators with output load current of 5A and 3A respectively. Their low dropout voltage (1.2V) and fast transient response make them an excellent solution for low voltage microprocessor applications.

The LMS1585AEP/87EP are available in adjustable versions, which can set the output voltage with only two external resistors. In addition, they are also available in 1.5V and 3.3V fixed voltage versions <sup>(1)</sup>.

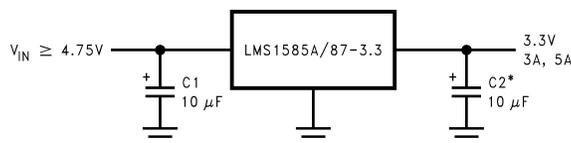
The LMS1585AEP/87EP circuits include a zener trimmed bandgap reference, current limiting and thermal shutdown.

The LMS1585AEP/87EP series are available in TO-220 and TO-263 packages.

### ENHANCED PLASTIC

- Extended Temperature Performance of -40°C to 125°C
- Baseline Control - Single Fab & Assembly Site
- Process Change Notification (PCN)
- Qualification & Reliability Data
- Solder (PbSn) Lead Finish is standard
- Enhanced Diminishing Manufacturing Sources (DMS) Support

### Typical Application



\* REQUIRED FOR STABILITY

### Connection Diagram

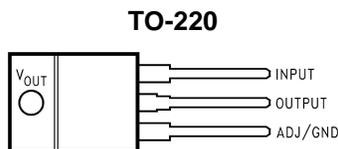


Figure 1. Top View

(1) Consult factory for other fixed voltage options.



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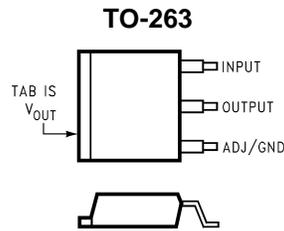
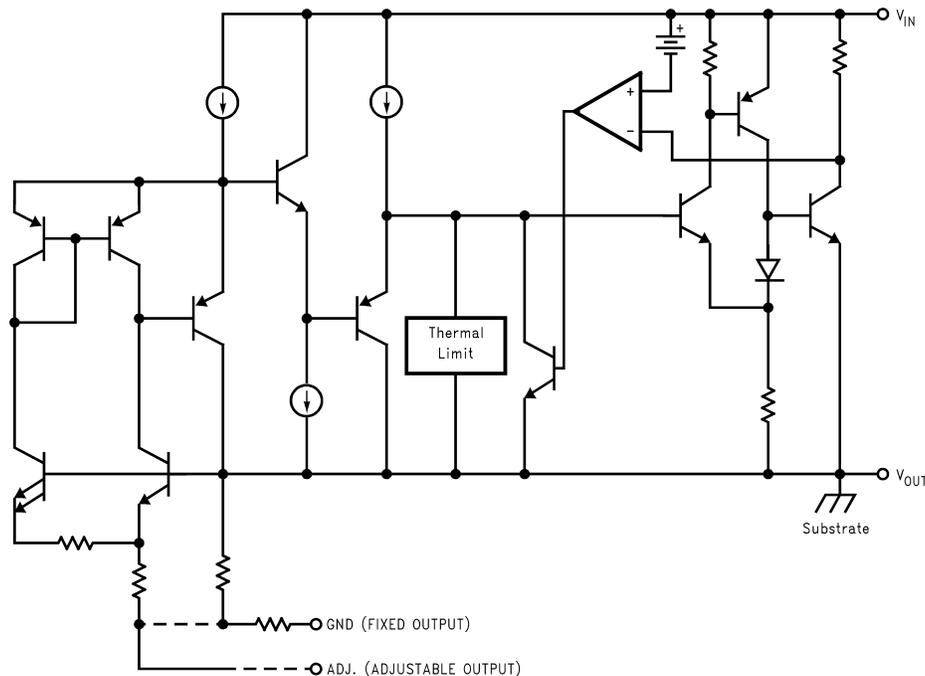


Figure 2. Top View

### Simplified Schematic



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

### Absolute Maximum Ratings <sup>(1)</sup>

Maximum Input to Output Voltage ( $V_{IN}$ to GND)	13V
Power Dissipation <sup>(2)</sup>	Internally Limited
Junction Temperature ( $T_J$ ) <sup>(2)</sup>	150°C
Storage Temperature Range	-65°C to 150°C
Lead Temperature	260°C, 10 sec
ESD Tolerance <sup>(3)</sup>	2000V

- (1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications and the test conditions, see the Electrical Characteristics.
- (2) The maximum power dissipation is a function of  $T_{J(max)}$ ,  $\theta_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any ambient temperature is  $P_D = (T_{J(max)} - T_A) / \theta_{JA}$ . All numbers apply for packages soldered directly into a PC board.
- (3) For testing purposes, ESD was applied using human body model, 1.5k $\Omega$  in series with 100pF.

## Electrical Characteristics

Typicals and limits appearing in normal type apply for  $T_J = 25^\circ\text{C}$ . Limits appearing in **Boldface** type apply over the entire junction temperature range for operation,  $0^\circ\text{C}$  to  $125^\circ\text{C}$  for commercial grade and  $-40^\circ\text{C}$  to  $125^\circ\text{C}$  for Enhanced Plastic.

Symbol	Parameter	Conditions	Min (1)	Typ (2)	Max (1)	Units
$V_{REF}$	Reference Voltage	LMS1585A-ADJEP $V_{IN}-V_{OUT} = 3\text{V}$ , $I_{OUT} = 10\text{mA}$ $10\text{mA} \leq I_{OUT} \leq 5\text{A}$ , $1.5\text{V} \leq V_{IN}-V_{OUT} \leq 5.75\text{V}$	1.238 <b>1.225</b>	1.25 <b>1.250</b>	1.262 <b>1.275</b>	V V
		LMS1587-ADJEP $10\text{mA} \leq I_{OUT} \leq 3\text{A}$ , $1.5\text{V} \leq V_{IN}-V_{OUT} \leq 5.75\text{V}$	<b>1.225</b>	1.250	<b>1.275</b>	V
$V_{OUT}$	Output Voltage	LMS1585A-1.5EP $I_{OUT} = 0\text{mA}$ , $V_{IN} = 5\text{V}$ $0 \leq I_{OUT} \leq 5\text{A}$ , $3\text{V} \leq V_{IN} \leq 7\text{V}$	1.485 <b>1.470</b>	1.500	1.515 <b>1.530</b>	V V
		LMS1585A-3.3EP $I_{OUT} = 0\text{mA}$ , $V_{IN} = 5\text{V}$ $0 \leq I_{OUT} \leq 5\text{A}$ , $4.75\text{V} \leq V_{IN} \leq 7\text{V}$	3.267 <b>3.235</b>	3.300 3.300	3.333 <b>3.365</b>	V V
		LMS1587-1.5EP $V_{IN} = 5\text{V}$ , $I_{OUT} = 0\text{mA}$ , $T_J = 25^\circ\text{C}$ $0 \leq I_{OUT} \leq 3\text{A}$ , $3\text{V} \leq V_{IN} \leq 7\text{V}$	1.485 <b>1.470</b>	1.500 1.500	1.515 <b>1.530</b>	V V
		LMS1587-3.3EP $0 \leq I_{OUT} \leq 3\text{A}$ , $4.75\text{V} \leq V_{IN} \leq 7\text{V}$	<b>3.235</b>	3.300	<b>3.365</b>	V
$\Delta V_{OUT}$	Line Regulation (3)	LMS1585AEP/87-ADJEP $I_{OUT} = 10\text{mA}$ , $2.75\text{V} \leq V_{IN} \leq 7\text{V}$		0.005	<b>0.2</b>	%
		LMS1585AEP/87-3.3EP $I_{OUT} = 0\text{mA}$ , $4.75\text{V} \leq V_{IN} \leq 7\text{V}$		0.005	<b>0.2</b>	%
		LMS1585AEP/87-1.5EP $I_{OUT} = 0\text{mA}$ , $3\text{V} \leq V_{IN} \leq 7\text{V}$		0.005	0.2	%
$\Delta V_{OUT}$	Load Regulation (3)	LMS1585A-ADJEP $V_{IN}-V_{OUT} = 3\text{V}$ , $10\text{mA} \leq I_{OUT} \leq 5\text{A}$		0.05	0.3 <b>0.5</b>	%
		LMS1585A-1.5EP/LMS1585A-3.3EP $V_{IN} = 5\text{V}$ , $0 \leq I_{OUT} \leq 5\text{A}$		0.05 0.05	0.3 <b>0.5</b>	%
		LMS1587-ADJEP $V_{IN}-V_{OUT} = 3\text{V}$ , $10\text{mA} \leq I_{OUT} \leq 3\text{A}$		0.05 0.05	0.3 <b>0.5</b>	%
		LMS1587-1.5EP/LMS1587-3.3EP $V_{IN} = 5\text{V}$ , $0 \leq I_{OUT} \leq 3\text{A}$		0.05 0.05	0.3 <b>0.5</b>	% %
$V_{IN}-V_{OUT}$	Dropout Voltage	LMS1585A-ADJEP/LMS1587-ADJEP $\Delta V_{REF} = 1\%$ , $I_{OUT} = 3\text{A}$		1.15	<b>1.3</b>	V
		LMS1585A-3.3EP/LMS1587-3.3EP/ LMS1585A-1.5EP/LMS1587-1.5EP $\Delta V_{OUT} = 1\%$ , $I_{OUT} = 3\text{A}$		1.15	<b>1.3</b>	V
		LMS1585A-ADJEP $\Delta V_{REF} = 1\%$ , $I_{OUT} = 5\text{A}$		1.2	<b>1.4</b>	V
		LMS1585A-1.5EP/LMS1585A-3.3EP $\Delta V_{OUT} = 1\%$ , $I_{OUT} = 5\text{A}$		1.2	<b>1.4</b>	V
$I_{LIMIT}$	Current Limit	LMS1585A-ADJEP/LMS1585A-3.3EP/ LMS1585A-1.5EP $V_{IN}-V_{OUT} = 5.5\text{V}$	<b>5.0</b>	6.6		A
		LMS1587-ADJEP/LMS1587-3.3EP/ LMS1587-1.5EP $V_{IN}-V_{OUT} = 5.5\text{V}$	<b>3.1</b>	4.3		A
	Minimum Load Current (4)	LMS1585AEP/87-ADJEP $1.5\text{V} \leq V_{IN}-V_{OUT} \leq 5.75\text{V}$		2.0	<b>10.0</b>	mA
	Quiescent Current	LMS1585A-3.3EP/LMS1587-3.3EP/ LMS1585A-1.5EP/LMS1587-1.5EP $V_{IN} = 5\text{V}$		7.0	<b>13.0</b>	mA

(1) All limits are guaranteed by testing or statistical analysis.

(2) Typical Values represent the most likely parametric norm.

(3) Load and line regulation are measured at constant junction temperature, and are guaranteed up to the maximum power dissipation of 30W. Power dissipation is determined by the input/output differential and the output current. Guaranteed maximum power dissipation will not be available over the full input/output range.

(4) The minimum output current required to maintain regulation.

## Electrical Characteristics (continued)

Typicals and limits appearing in normal type apply for  $T_J = 25^\circ\text{C}$ . Limits appearing in **Boldface** type apply over the entire junction temperature range for operation,  $0^\circ\text{C}$  to  $125^\circ\text{C}$  for commercial grade and  $-40^\circ\text{C}$  to  $125^\circ\text{C}$  for Enhanced Plastic.

Symbol	Parameter	Conditions	Min (1)	Typ (2)	Max (1)	Units
	Thermal Regulation	$T_A = 25^\circ\text{C}$ , 30ms Pulse		0.003		%/W
	Ripple Rejection	LMS1585A-ADJEP $f_{\text{RIPPLE}} = 120\text{Hz}$ , $V_{\text{IN}} - V_{\text{OUT}} = 3\text{V}$ , $I_{\text{OUT}} = 5\text{A}$ , $C_{\text{OUT}} = 25\mu\text{F}$ Tantalum		72		dB
		LMS1585A-1.5EP $f_{\text{RIPPLE}} = 120\text{Hz}$ , $C_{\text{OUT}} = 25\mu\text{F}$ Tantalum, $I_{\text{OUT}} = 5\text{A}$ , $V_{\text{IN}} = 4.5\text{V}$	<b>60</b>	72		dB
		LMS1585A-3.3EP $f_{\text{RIPPLE}} = 120\text{Hz}$ , $C_{\text{OUT}} = 25\mu\text{F}$ Tantalum, $I_{\text{OUT}} = 5\text{A}$ , $V_{\text{IN}} = 6.3\text{V}$		72		dB
		LMS1587-ADJEP $f_{\text{RIPPLE}} = 120\text{Hz}$ , $V_{\text{IN}} - V_{\text{OUT}} = 3\text{V}$ , $I_{\text{OUT}} = 3\text{A}$ $C_{\text{OUT}} = 25\mu\text{F}$ Tantalum		72		dB
		LMS1587-1.5EP $f_{\text{RIPPLE}} = 120\text{Hz}$ , $C_{\text{OUT}} = 25\mu\text{F}$ Tantalum, $I_{\text{OUT}} = 3\text{A}$ , $V_{\text{IN}} = 4.5\text{V}$	<b>60</b>	72		dB
		LMS1587-3.3EP $f_{\text{RIPPLE}} = 120\text{Hz}$ , $C_{\text{OUT}} = 25\mu\text{F}$ Tantalum, $I_{\text{OUT}} = 3\text{A}$ , $V_{\text{IN}} = 6.3\text{V}$		72		dB
	Adjust Pin Current			55	<b>120</b>	$\mu\text{A}$
	Adjust Pin Current	$10\text{mA} \leq I_{\text{OUT}} \leq I_{\text{FULLLOAD}}$ , $1.5\text{V} \leq V_{\text{IN}} - V_{\text{OUT}} \leq 5.75\text{V}$ (5)		0.2		$\mu\text{A}$
	Temperature Stability			0.5		%
	Long Term Stability	$T_A = 125^\circ\text{C}$ , 1000Hrs		0.03		%
	RMS Output Noise (% of $V_{\text{OUT}}$ )	$10\text{Hz} \leq f \leq 10\text{kHz}$		0.003		%
	Thermal Resistance Junction-to-Case	3-Lead TO-263: Control/Output Section 3-Lead TO-220: Control/Output Section			0.65/2.7 0.65/2.7	$^\circ\text{C}/\text{W}$ $^\circ\text{C}/\text{W}$

(5)  $I_{\text{FULLLOAD}}$  is 5A for LMS1585AEP and 3A for LMS1587EP.

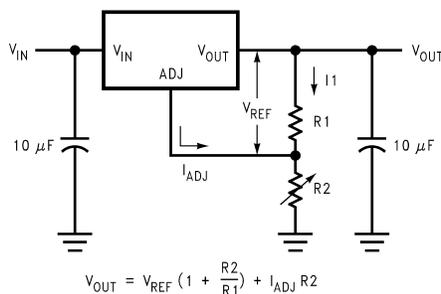
## Application Note

### Output Voltage

The adjustable version develops at 1.25V reference voltage, ( $V_{\text{REF}}$ ), between the output and the adjust terminal. As shown in [Figure 3](#), this voltage is applied across resistor R1 to generate a constant current I1. This constant current then flows through R2. The resulting voltage drop across R2 adds to the reference voltage to sets the desired output voltage.

The current  $I_{\text{ADJ}}$  from the adjustment terminal introduces an output error. But since it is small ( $120\mu\text{A}$  max), it becomes negligible when R1 is in the  $100\Omega$  range.

For fixed voltage devices, R1 and R2 are integrated inside the devices.



**Figure 3. Basic Adjustable Regulator**

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