Bi-CMOS IC

LV1150



Virtual Surround System IC

Overview

The LV1150 is a virtual surround system Bi-CMOS IC for video soundtracks and audio.

The main feature of this IC is the ability to create an audio ambience equivalent to that of a multichannel system by adding a signal to which virtual surround processing has been applied to the left and right channel input signals. It furthermore allows modification of this effect by the use of L+R and L–R passive matrix processing and adjustment of the surround processing level with a level control.

Functions and Features

- Virtual surround function
- Passive matrix: L+R, L-R
- Adjustable surround effect level
- Bypass and virtual surround (L+R, L-R) switching function
- Output filters are provided on chip.
- On-chip V_{DD} circuit
- ADM technique based A/D and D/A converters
- Simulated stereo for monaural input signals
- Package: DIP24S

Specifications Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		12	V
Allowable power dissipation	Pdmax	$Ta \le 70^{\circ}C$ * With printed circuit board	700	W
Operating temperature	Topr		-20 to +70	°C
Storage temperature	Tstg		-40 to +125	°C

Note: * Printed circuit board size: 114.3 × 76.1 mm, t = 1.6 mm. Material: Glass epoxy.

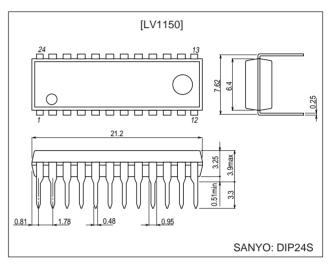
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Package Dimensions

unit: mm

3067-DIP24S



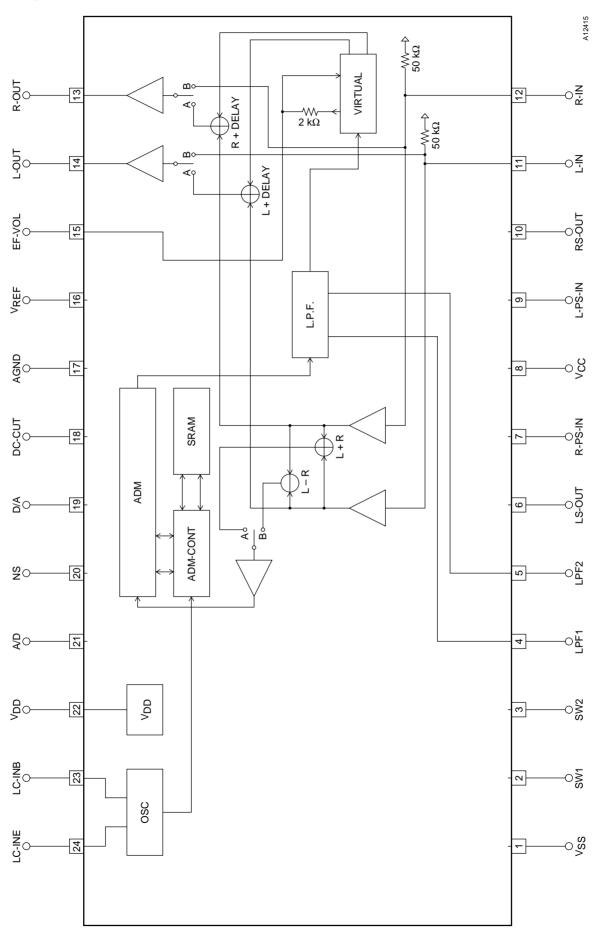
Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V _{CC}		7	V
Operating supply voltage range	V _{CC} opr		6.5 to 10	V

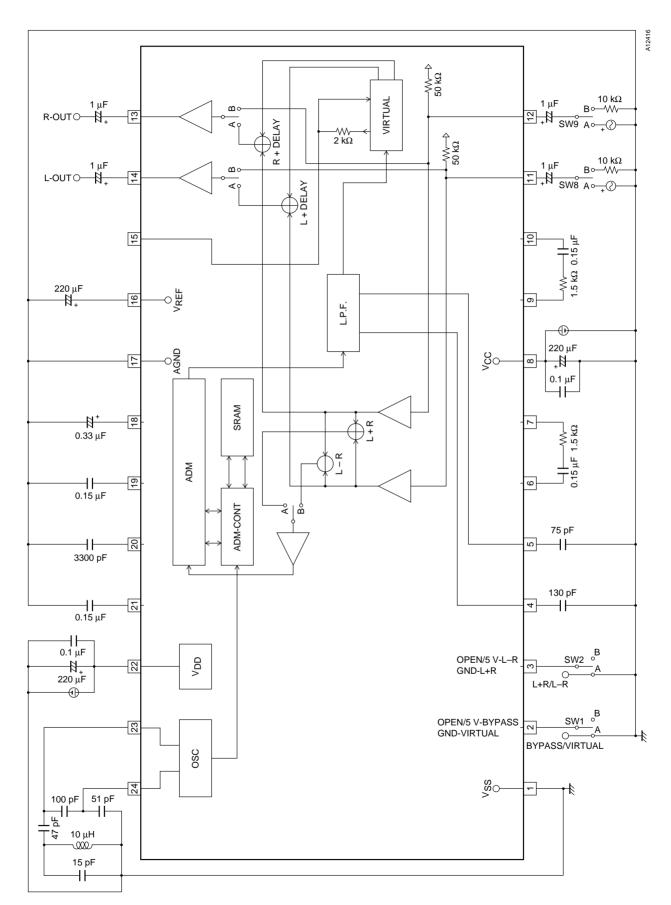
Electrical Characteristics at Ta = 25° C, V_{CC} = 7.0 V, V_{IN} = -10 dBm, f = 1 kHz, in bypass mode

Parameter	Symbol	Conditions		Ratings		Unit
		Conditions	min	typ	max	Unit
Quiescent current	I _{CC}		15	40	60	mA
Output noise voltage	V _{NO}	Rs = 10 kΩ, JIS A		-110	-90	dBm
		In virtual surround mode		-88	-80	dBm
I/O signal level deviation	Vo	$V_{IN} = -10 \text{ dBm} = 0 \text{ dB}$	-2	0	+2	dB
Total harmonic distortion	THD	400 Hz to 30 kHz bandpass filter		0.005	0.03	%
		In virtual surround mode		0.13	1.0	%
Headroom	H∙R	$V_{IN} = -10 \text{ dBm} = 0 \text{ dB}, \text{ THD} = 1\%$	10	15		dB
headiooni		In virtual surround mode	10	12		dB

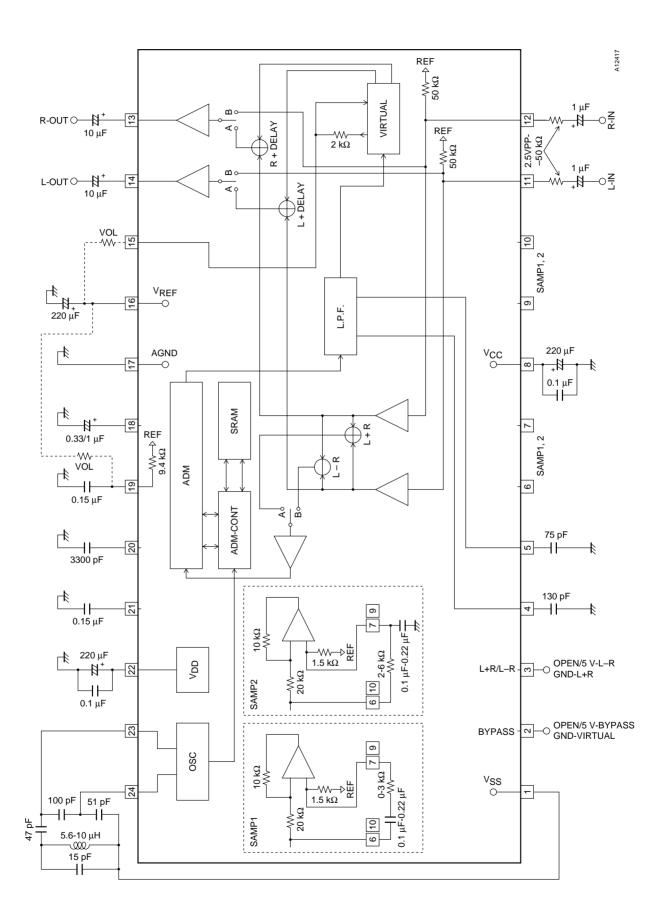
Block Diagram



Test Circuit Example



Application Circuit Example



Operating Principles

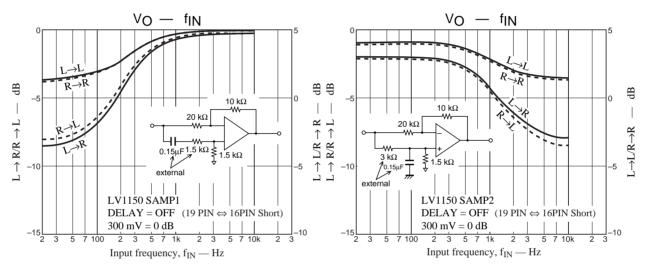
1. Modes

The mode can be set using DIP switches on pins 2 and 3.

- Bypass/virtual: switches between bypass and virtual modes.
- L+R/L-R: Switches the virtual mode effect.
- Since this switching is independent of the bypass function, it has no effect in bypass mode.

2. Other notes

- The level of the virtual effect can be changed by the values of the external resistors connected to pins 15 and 19. (See the sample application circuit diagram.) Note that the effect is maximum when these pins are open.
- There are two options that may be attached to pins 6 and 7 and pins 9 and 10. (See the sample application circuit diagram.) High boost (SAMP1) and low boost (SAMP2) effects can be acquired using external circuits on these pins. (See the charts.)

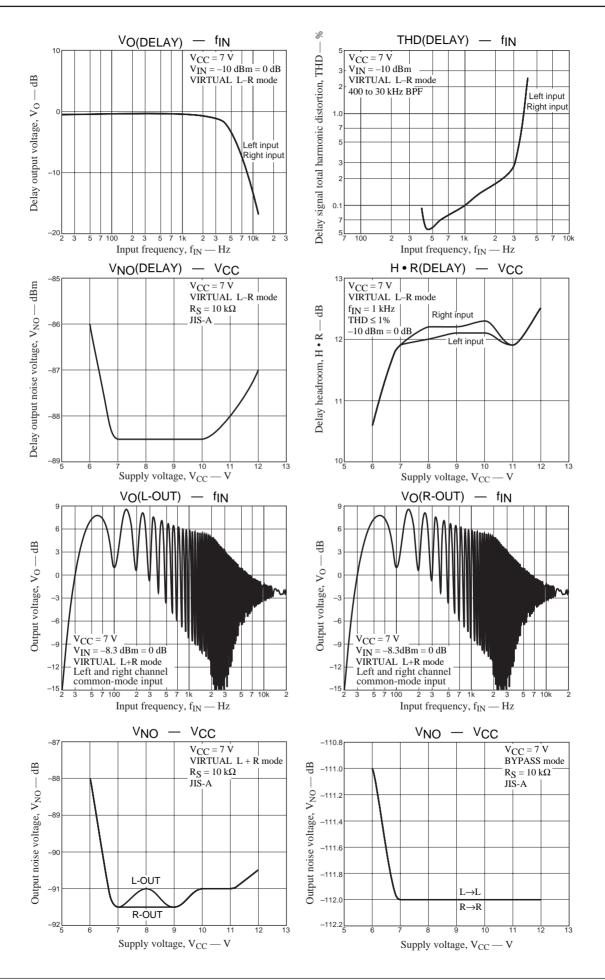


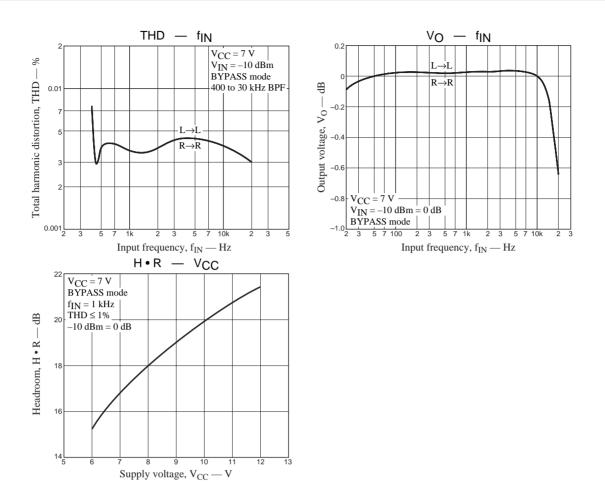
Pin Descriptions

Pin No.	Pin	Pin voltage (V)	Function	Equivalent circuit
2	SW1	0/0.7	Bypass/virtual switching	2 <u>1 kΩ</u>
3	SW1	0.0.1	L+R/L-R switching	(3) //// A12418
4 5	LPF1 LPF2	1/2V _{CC}	Low-pass filter capacitor connection	BIAS 4 5 Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ
6 10	LS-OUT RS-OUT	1/2V _{CC}	Surround signal outputs	6 100 Ω BIAS 10 A12420
7 9	R-PS-IN L-PS-IN	1/2V _{CC}	Virtual surround processing signal inputs.	7 500 Ω 9 5

Continued from preceding page.

Pin No.	Pin	Pin voltage	Function	Equivalent circuit	
11 12	L-IN R-IN	1/2V _{CC}	Signal inputs	$\begin{array}{c} 11 \\ 12 \\ 12 \\ 12 \\ 12 \\ 12 \\ 12 \\ 12 $	A12422
13 14	R-OUT L-OUT	1/2V _{CC}	Signal outputs	UCC 13 100 Ω BIAS 14 	A12423
15	EF-VOL	1/2V _{CC}	Virtual surround control	2 kΩ 500 Ω (15)	A12424
16	V _{REF}	1/2V _{CC}	V _{REF} amplifier reference		A12425
18	DC-CUT	1/2V _{CC}	DC cut capacitor connection		A12426
19 21	D/A A/D	1/2V _{CC}	A/D (D/A) converter integrator capacitor connection	$ \begin{array}{c} $	A12427
20	NS	1/2V _{CC}	A/D noise shaper capacitor connection		A12428
23 24	LC-INB LC-INE	0/5V	Clock control	$\begin{array}{c} 15 \text{ k}\Omega \\ \hline \\ $	A12429





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