

SCHOTTKY RECTIFIER
New GenIII D-61 Package

80 Amp

Major Ratings and Characteristics




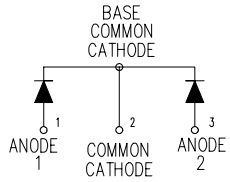
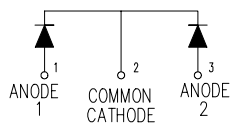
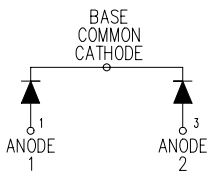
Characteristics	Values	Units
$I_{F(AV)}$ Rectangular waveform	80	A
V_{RRM}	20	V
I_{FSM} @tp=5 μ s sine	6000	A
V_F @40Apk, $T_J=125^\circ\text{C}$ (perleg)	0.32	V
T_J range	-55to150	$^\circ\text{C}$

Description/Features

The center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for 3.3V output power supplies. The proprietary barrier technology allows for reliable operation up to 150 $^\circ\text{C}$ junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

- 150 $^\circ\text{C}$ T_J operation
- Center tap module
- Optimized for 3.3V application
- Ultra low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- *New fully transfer-mold low profile, small footprint, high current package*

Case Styles

83CNQ...A	83CNQ...ASM	83CNQ...ASL
		
 <p>BASE COMMON CATHODE</p> <p>1 ANODE 1 2 COMMON CATHODE 3 ANODE 2</p> <p>D61-8</p>	 <p>1 ANODE 1 2 COMMON CATHODE 3 ANODE 2</p> <p>D61-8-SM</p>	 <p>BASE COMMON CATHODE</p> <p>1 ANODE 1 2 ANODE 2 3 COMMON CATHODE</p> <p>D61-8-SL</p>

87CNQ020A

Bulletin PD-20045 rev. A 09/01

International
IRF Rectifier

Voltage Ratings

Part number	87CNQ020A / ..020ASM / ..020ASL	
V _R Max. DC Reverse Voltage (V)	@ 125 °C	20
V _R Max. DC Reverse Voltage (V)	@ 150 °C	10

Absolute Maximum Ratings

Parameters	87CNQ	Units	Conditions
I _{F(AV)} Max. Average Forward Current (Per Device) Current (Per Leg)	80 40	A	50% duty cycle @ T _C = 135 °C, rectangular waveform
I _{FSM} Max. Peak One Cycle Non-Repetitive Surge Current (Per Leg)	6000 1100	A	5 μs Sine or 3 μs Rect. pulse 10ms Sine or 6ms Rect. pulse Following any rated load condition and with rated V _{RRM} applied
E _{AS} Non-Repetitive Avalanche Energy (Per Leg)	36	mJ	T _J = 25 °C, I _{AS} = 8 Amps, L = 1.12 mH
I _{AR} Repetitive Avalanche Current (Per Leg)	8	A	Current decaying linearly to zero in 1 μsec Frequency limited by T _J max. V _A = 1.5 x V _R typical

Electrical Specifications

Parameters	87CNQ	Units	Conditions
V _{FM} Max. Forward Voltage Drop (Per Leg) (1)	0.45	V	@ 40A T _J = 25 °C
	0.51	V	@ 80A T _J = 25 °C
	0.32	V	@ 40A T _J = 125 °C
	0.39	V	@ 80A T _J = 125 °C
	0.29	V	@ 40A T _J = 150 °C
	0.37	V	@ 80A T _J = 150 °C
I _{RM} Max. Reverse Leakage Current (Per Leg) (1)	5.5	mA	T _J = 25 °C V _R = rated V _R
	550	mA	T _J = 125 °C V _R = rated V _R
	90	mA	T _J = 125 °C V _R = 5V
	70	mA	T _J = 125 °C V _R = 3.3V
	480	mA	T _J = 150 °C V _R = 10V
V _{F(TO)} Threshold Voltage	0.191	V	T _J = T _J max.
r _t Forward Slope Resistance	2.3	mΩ	
C _T Max. Junction Capacitance (Per Leg)	6500	pF	V _R = 5V _{DC} , (test signal range 100Khz to 1Mhz) 25 °C
L _S Typical Series Inductance (Per Leg)	5.5	nH	Measured lead to lead 5mm from package body
dv/dt Max. Voltage Rate of Change (Rated V _R)	10000	V/ μs	

Thermal-Mechanical Specifications

(1) Pulse Width < 300 μs, Duty Cycle < 2%

Parameters	87CNQ	Units	Conditions
T _J Max. Junction Temperature Range	-55 to 150	°C	
T _{stg} Max. Storage Temperature Range	-55 to 150	°C	
R _{thJC} Max. Thermal Resistance Junction to Case (Per Leg)	0.85	°C/W	DC operation
R _{thJC} Max. Thermal Resistance Junction to Case (Per Package)	0.42	°C/W	DC operation
R _{thCS} Typical Thermal Resistance, Case to Heatsink (D61-8 Only)	0.30	°C/W	Mounting surface, smooth and greased Device flatness < 5 mils
wt Approximate Weight	7.8(0.28)	g(oz.)	
T Mounting Torque (D61-8 Only)	Min.	40(35)	Kg-cm (lbf-in)
	Max.	58(50)	

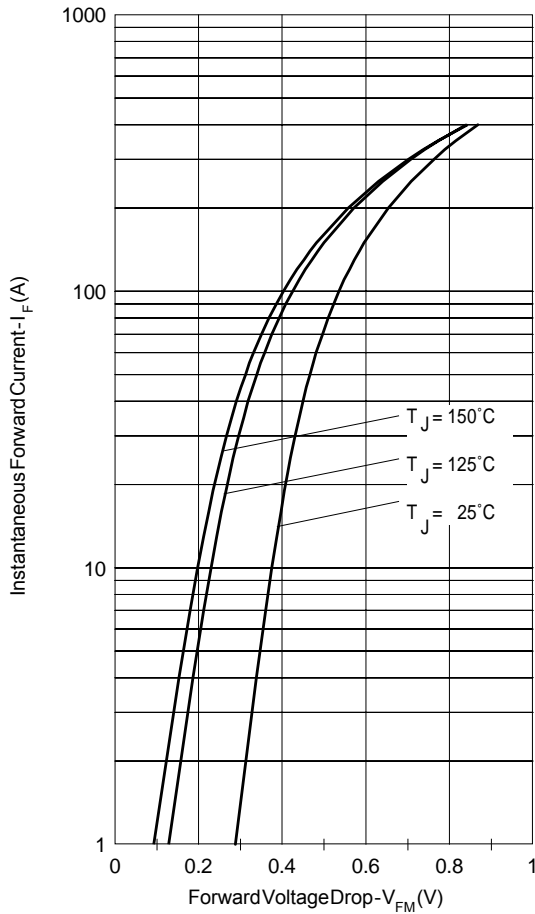


Fig. 1 - Max. Forward Voltage Drop Characteristics (Per Leg)

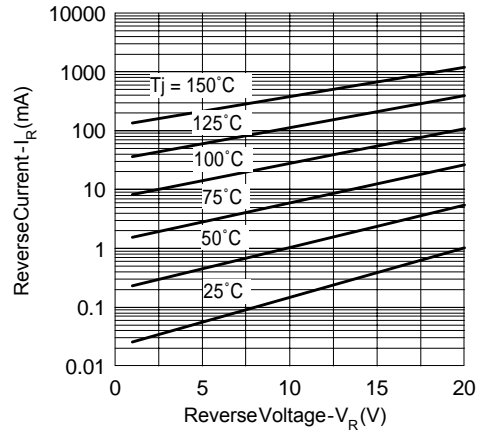


Fig. 2 - Typical Values Of Reverse Current Vs. Reverse Voltage (Per Leg)

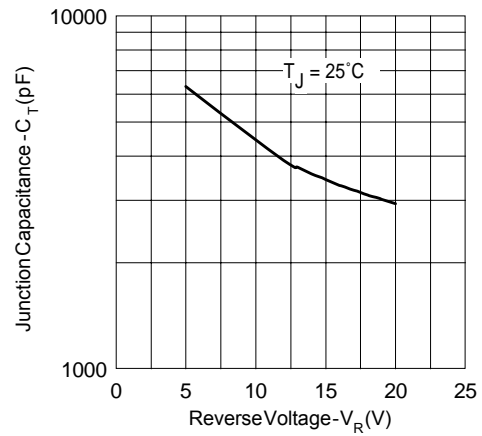


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

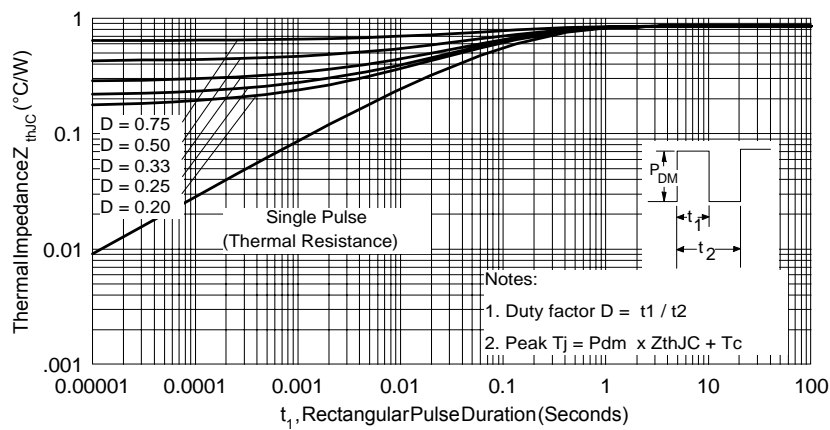


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics (Per Leg)

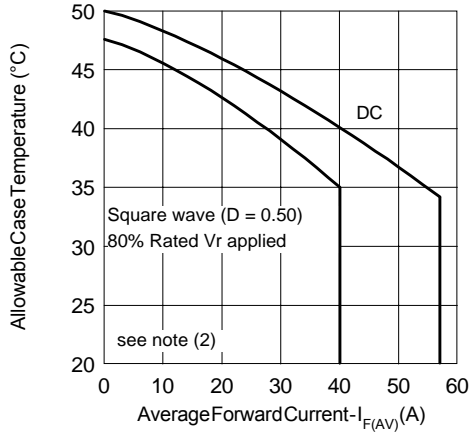


Fig. 5- Max. Allowable Case Temperature Vs. Average Forward Current (PerLeg)

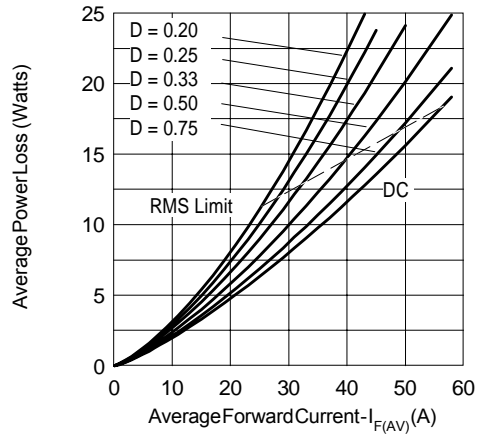


Fig. 6- Forward Power Loss Characteristics (PerLeg)

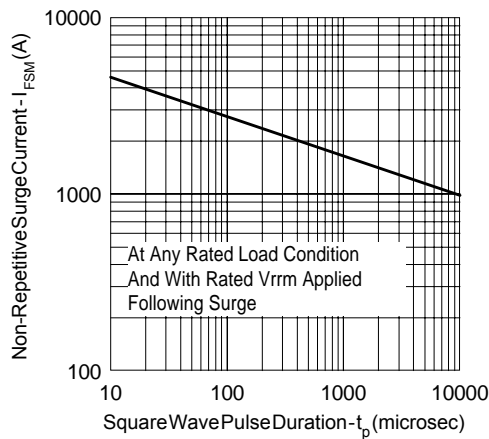


Fig. 7- Max. Non-Repetitive Surge Current (PerLeg)

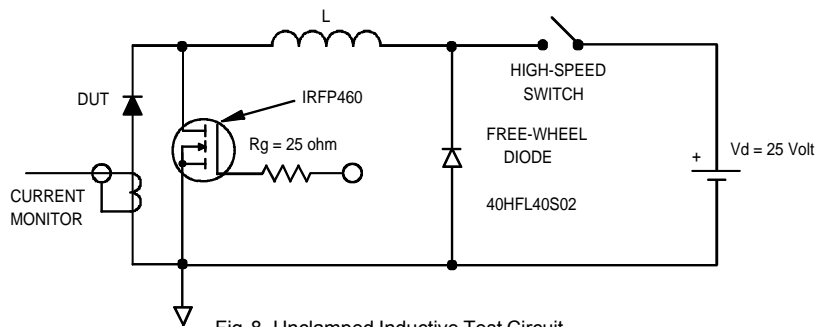


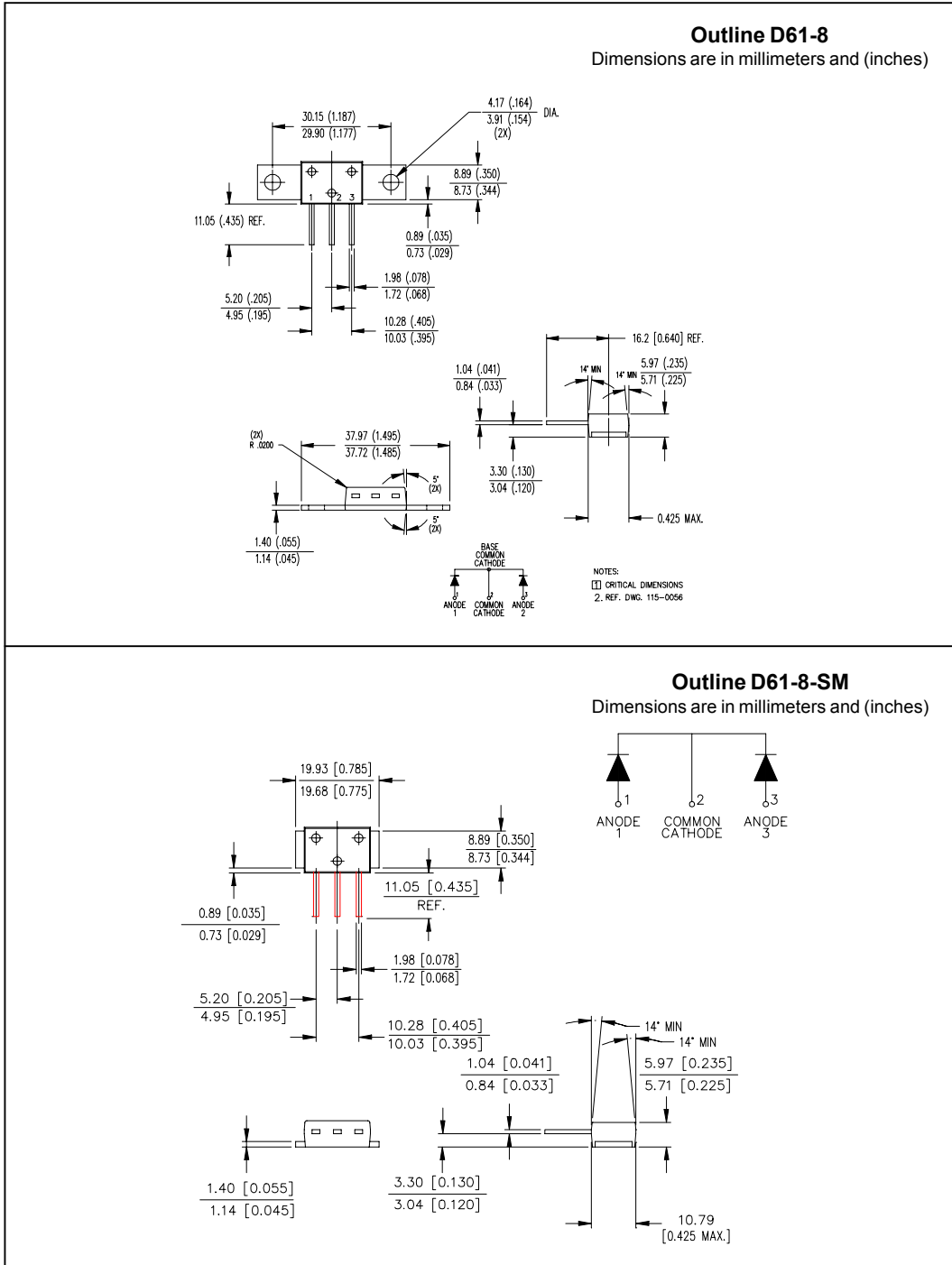
Fig. 8- Unclamped Inductive Test Circuit

(2) Formula used: $T_c = T_j - (P_d + P_{d_{REV}}) \times R_{thJC}$;

P_d = Forward Power Loss = $I_{F(AV)} \times V_{FM} @ (I_{F(AV)} / D)$ (see Fig. 6);

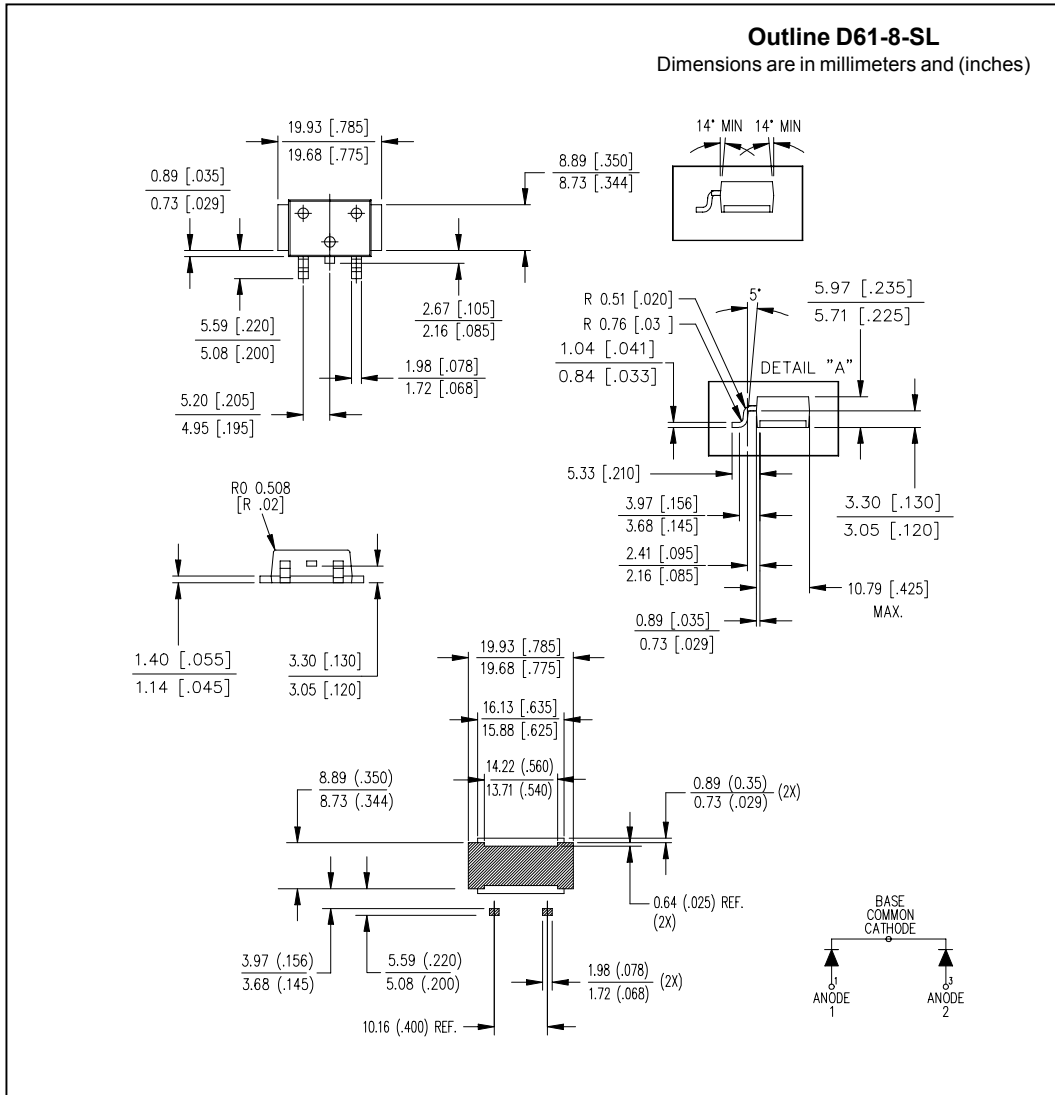
$P_{d_{REV}}$ = Inverse Power Loss = $V_{R1} \times I_R (1 - D)$; $I_R @ V_{R1} = 80\%$ rated V_R

Outline Table



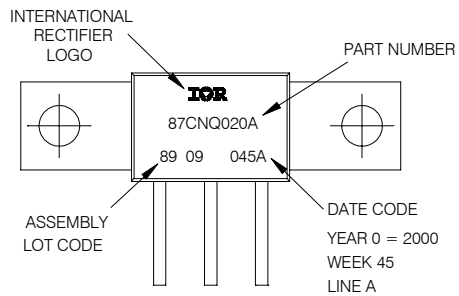
Outline D61-8-SM
 Dimensions are in millimeters and (inches)

Outline Table



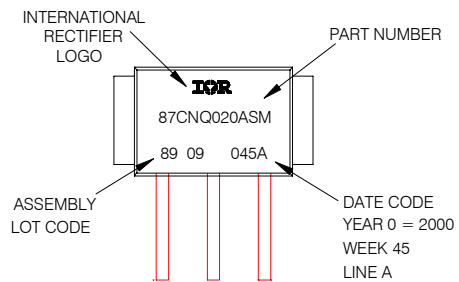
Part Marking Information

EXAMPLE: THIS IS A 87CNQ020A WITH
 LOT CODE 89 09
 ASSEMBLED ON WW 45, 2000
 IN THE ASSEMBLY LINE "A"



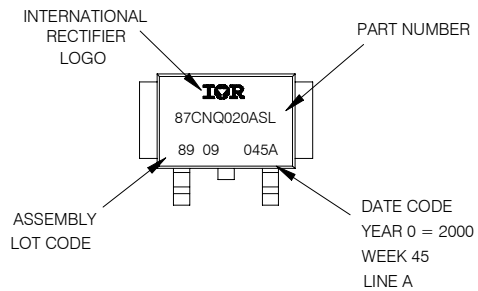
D61-8

EXAMPLE: THIS IS A 87CNQ020ASM WITH
 LOT CODE 89 09
 ASSEMBLED ON WW 45, 2000
 IN THE ASSEMBLY LINE "A"



D61-8-SM

EXAMPLE: THIS IS A 87CNQ020ASL WITH
 LOT CODE 89 09
 ASSEMBLED ON WW 45, 2000
 IN THE ASSEMBLY LINE "A"



D61-8-SL

87CNQ020A

Bulletin PD-20045 rev. A 09/01

International
IOR Rectifier

Data and specifications subject to change without notice.
This product has been designed and qualified for Industrial Level.
Qualification Standards can be found on IR's Web site.

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