

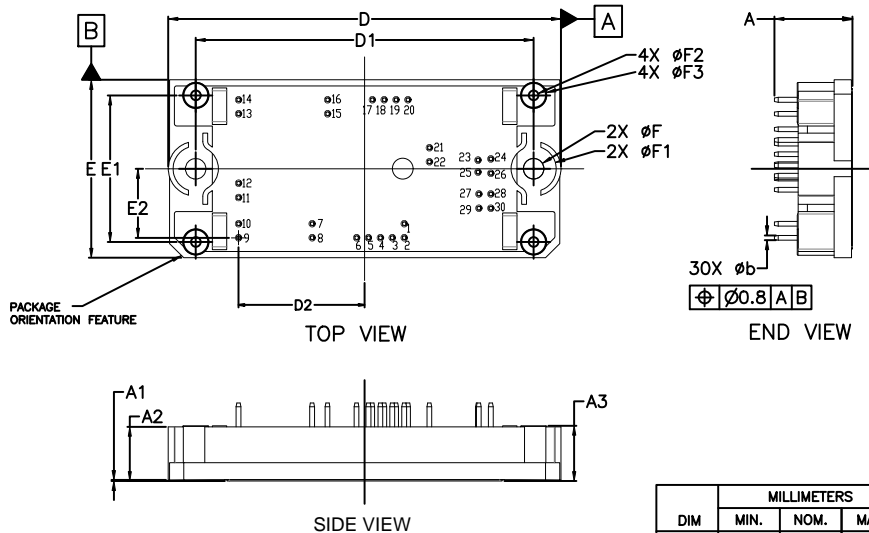
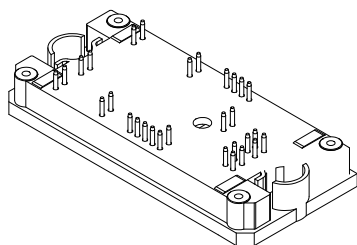
MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

ON Semiconductor®

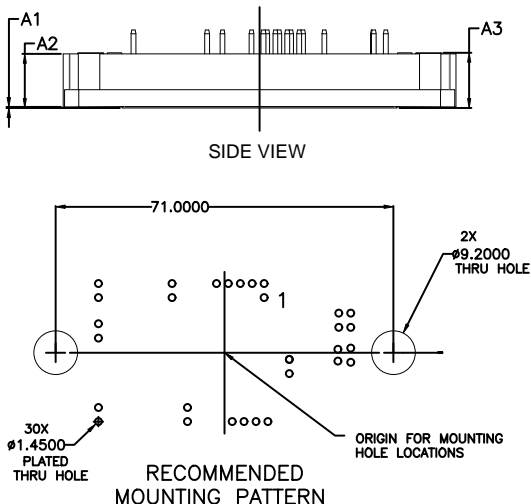


PIM30, 71x37.4
CASE 180AQ
ISSUE A

DATE 25 JUN 2018



| MOUNTING HOLE POSITION | | | MOUNTING HOLE POSITION | | |
|------------------------|--------|--------|------------------------|--------|--------|
| PIN | X | Y | PIN | X | Y |
| 1 | 8.30 | 11.55 | 16 | -7.800 | -14.50 |
| 2 | 8.30 | 14.50 | 17 | 1.60 | -14.50 |
| 3 | 5.80 | 14.50 | 18 | 4.10 | -14.50 |
| 4 | 3.30 | 14.50 | 19 | 6.60 | -14.50 |
| 5 | 0.80 | 14.50 | 20 | 9.10 | -14.50 |
| 6 | -1.70 | 14.50 | 21 | 13.60 | -4.40 |
| 7 | -11.05 | 11.55 | 22 | 13.60 | -1.45 |
| 8 | -11.05 | 14.50 | 23 | 23.80 | -1.80 |
| 9 | -26.50 | 14.50 | 24 | 26.50 | -2.05 |
| 10 | -26.50 | 11.55 | 25 | 23.80 | 0.70 |
| 11 | -26.50 | 6.05 | 26 | 26.50 | 0.95 |
| 12 | -26.50 | 3.05 | 27 | 24.00 | 5.30 |
| 13 | -26.50 | -11.55 | 28 | 26.50 | 5.30 |
| 14 | -26.50 | -14.50 | 29 | 24.00 | 8.30 |
| 15 | -7.80 | -11.55 | 30 | 26.50 | 8.30 |



| DIM | MILLIMETERS | | |
|-----|-------------|-------|-------|
| | MIN. | NOM. | MAX. |
| A | 15.90 | 16.40 | 16.90 |
| A1 | --- | 0.30 | 0.60 |
| A2 | 10.90 | 11.40 | 11.90 |
| A3 | 11.10 | 11.60 | 12.10 |
| b | 0.90 | 1.00 | 1.10 |
| D | 82.00 | 82.50 | 83.00 |
| D1 | 70.50 | 71.00 | 71.50 |
| D2 | 26.50 REF | | |
| E | 36.90 | 37.40 | 37.90 |
| E1 | 30.30 | 30.80 | 31.30 |
| E2 | 14.50 REF | | |
| F | 4.30 | 4.30 | 4.50 |
| F1 | 9.5 REF | | |
| F2 | 2.0 REF | | |
| F3 | 5.5 REF | | |

NOTE 4

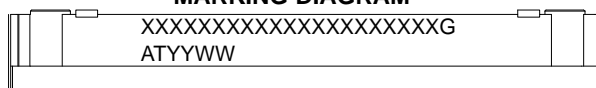
| PIN | PIN POSITION | | PIN | PIN POSITION | |
|-----|--------------|--------|-----|--------------|-------|
| | X | Y | | X | Y |
| 1 | 8.30 | -11.55 | 16 | -7.800 | 14.50 |
| 2 | 8.30 | -14.50 | 17 | 1.60 | 14.50 |
| 3 | 5.80 | -14.50 | 18 | 4.10 | 14.50 |
| 4 | 3.30 | -14.50 | 19 | 6.60 | 14.50 |
| 5 | 0.80 | -14.50 | 20 | 9.10 | 14.50 |
| 6 | -1.70 | -14.50 | 21 | 13.60 | 4.40 |
| 7 | -11.05 | -11.55 | 22 | 13.60 | 1.45 |
| 8 | -11.05 | -14.50 | 23 | 23.80 | 1.80 |
| 9 | -26.50 | -14.50 | 24 | 26.50 | 2.05 |
| 10 | -26.50 | -11.55 | 25 | 23.80 | -0.70 |
| 11 | -26.50 | -6.05 | 26 | 26.50 | -0.95 |
| 12 | -26.50 | -3.05 | 27 | 24.00 | -5.30 |
| 13 | -26.50 | 11.55 | 28 | 26.50 | -5.30 |
| 14 | -26.50 | 14.50 | 29 | 24.00 | -8.30 |
| 15 | -7.80 | 11.55 | 30 | 26.50 | -8.30 |

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSIONS b AND b1 APPLY TO THE PLATED TERMINALS AND ARE MEASURED AT DIMENSION A4.
4. POSITION OF THE CENTER OF THE TERMINALS IS DETERMINED FROM DATUM B THE CENTER OF DIMENSION D, X DIRECTION, AND FROM DATUM A, Y DIRECTION. POSITIONAL TOLERANCE, AS NOTED IN DRAWING, APPLIES TO EACH TERMINAL IN BOTH DIRECTIONS.
5. PACKAGE MARKING IS LOCATED AS SHOWN ON THE SIDE OPPOSITE THE PACKAGE ORIENTATION FEATURES.

GENERIC

MARKING DIAGRAM*



XXXXX = Specific Device Code
G = Pb-Free Package
AT = Assembly & Test Site Code
YYWW = Year and Work Week Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

| | | |
|-------------------------|-----------------------|--|
| DOCUMENT NUMBER: | 98AON86769G | Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| DESCRIPTION: | PIM30, 71x37.4 | PAGE 1 OF 1 |

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.