

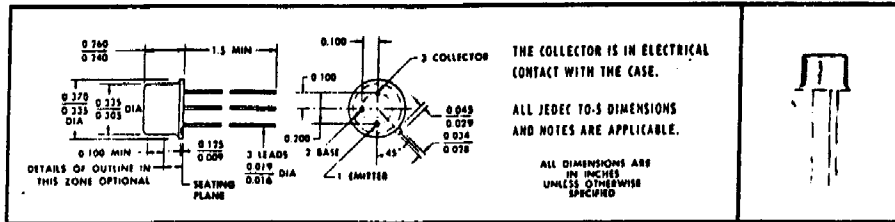
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<h1 style="margin: 0;">2N1564 - 2N1565</h1> <h1 style="margin: 0;">2N1566</h1>	<h1 style="margin: 0;">Diffused Silicon</h1> <h1 style="margin: 0;">NPN Transistors</h1>
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mechanical data



MAXIMUM RATINGS		600mW
Total Device Dissipation - Free Air		-65°C to +175°C
Operating Temperature		-65°C to +200°C
Storage Temperature		

DESIGN CHARACTERISTICS AT 25°C (Except as Noted)										
SYMBOL	PARAMETER	TEST CONDITIONS	2N1564		2N1565		2N1566		UNITS	
			MIN.	TYP. MAX.	MIN.	TYP. MAX.	MIN.	TYP. MAX.		
V_{CB0}	Collector-Base Breakdown Voltage	$I_{CB} = 10\mu A, I_E = 0$	80	100	-	80	100	-	V	
$^*BV_{CE0}$	Collector-Emitter Breakdown Voltage	$I_{CE} = 10mA, I_B = 0$	60	75	-	60	75	-	V	
BV_{EB0}	Emitter-Base Breakdown Voltage	$I_{EB} = 100\mu A, I_C = 0$	5	8	-	5	8	-	V	
I_{CBO}	Collector Reverse Current	$V_{CB} = 40V, T = 25^\circ C$	-	0.05	1	-	0.05	1	μA	
I_{CBO}	Collector Reverse Current	$V_{CB} = 40V, T = 150^\circ C$	-	5	100	-	5	100	μA	
I_{EBO}	Emitter Reverse Current	$V_{EB} = 5V, T = 25^\circ C$	-	1	10	-	1	10	μA	
$V_{CE(sat)}$	Saturation Voltage	$I_B = 2mA, I_C = 10mA$	-	.6	1	-	.6	1	V	
C_{ob}	Collector Capacitance	$V_{CB} = 5V, I_E = 0, f = 1mc$	-	5	10	-	5	10	μfd	
h_{fe}	Current Transfer Ratio	$V_{CE} = 5V, I_E = -5mA, f = 1KC$	20	-	50	40	-	100	80 - 200	-
h_{fe}	Current Transfer Ratio	$V_{CE} = 5V, I_E = -1mA, f = 1KC$	15	-	-	30	-	-	60	-
h_{fe}	Current Transfer Ratio	$V_{CE} = 5V, I_E = -5mA, f = 1KC, T = -55^\circ C$	12	-	-	20	-	-	40	-
h_{ie}	Input Impedance	$V_{CE} = 5V, I_E = -5mA, f = 1KC$	-	-	1200	-	-	1500	-	1800 Ω
h_{oe}	Output Admittance	$V_{CE} = 5V, I_E = -5mA, f = 1KC$	-	25	-	-	65	-	95	$\mu mhos$
h_{re}	Voltage Feedback Transfer Ratio	$V_{CE} = 5V, I_E = -5mA, f = 1KC$	-	.9	-	-	1.1	-	1.3	$\times 10^{-4}$
h_{fe1}	Current Transfer Ratio	$V_{CE} = 5V, I_E = -5mA, f = 30mc$	1	-	-	2	-	-	2	-

*Pulse measurement 300 μ sec pulse, 2% Duty Cycle

