

2N3423 • 2N3424

DUAL NPN LOW NOISE SENSE AND HIGH FREQUENCY DIFFERENTIAL AMPLIFIERS DIFFUSED SILICON PLANAR EPITAXIAL TRANSISTORS

- $h_{FE1} \dots 10\%$ @ 3.0 mA
- h_{FE2}
- $|V_{BE1} - V_{BE2}| \dots 5.0$ mV (MAX) @ 3.0 mA
- $|\Delta(V_{BE1} - V_{BE2})| \dots 20$ μ V/ $^{\circ}$ C (MAX) @ 3.0 mA, -55° C to $+125^{\circ}$ C
- $f_T \dots 600$ MHz (MIN) @ 4.0 mA
- NF $\dots 3.5$ dB (TPY) @ 60 Hz

ABSOLUTE MAXIMUM RATINGS (Note 1)

Maximum Temperatures

Storage Temperature

Operating Junction Temperature

Lead Temperature (60 seconds)

Maximum Power Dissipation (Notes 2 & 3)

Total Dissipation at 25° C Case Temperature

at 100° C Case Temperature

at 25° C Ambient Temperature

Maximum Voltages and Current

V_{CBO} Collector to Base Voltage

V_{CEO} Collector to Emitter Voltage (Note 4)

V_{EBO} Emitter to Base Voltage

I_C Collector Current

V_{C1C2} Collector₁ to Collector₂ Voltage

Voltage Rating Any Lead to Case

-65° C to $+200^{\circ}$ C

200° C

300° C

One Side

0.6 W

0.25 W

0.3 W

Both Sides

1.2 W

0.5 W

0.45 W

30 V

15 V

3.0 V

50 mA

± 200 V

± 200 V

See TO5-9 Package Outline



MATCHING CHARACTERISTICS (25° C Ambient Temperature unless otherwise noted)

SYMBOL	CHARACTERISTIC	2N3423		2N3424		UNITS	TEST CONDITIONS
		MIN.	MAX.	MIN.	MAX.		
h_{FE1} h_{FE2}	DC Current Gain Ratio (Note 5)	0.8	1.0	0.9	1.0		$I_C = 3.0$ mA, $V_{CE} = 3.0$ V
$ V_{BE1} - V_{BE2} $	Base to Emitter Voltage Differential		10		5.0	mV	$I_C = 3.0$ mA, $V_{CE} = 3.0$ V
$ \Delta(V_{BE1} - V_{BE2}) $	Base to Emitter Voltage Differential Change		3.2 (40 μ V/ $^{\circ}$ C)		1.6 (20 μ V/ $^{\circ}$ C)	mV	$I_C = 3.0$ mA, $V_{CE} = 3.0$ V, $T_A = -55^{\circ}$ C to $+25^{\circ}$ C
$ \Delta(V_{BE1} - V_{BE2}) $	Base to Emitter Voltage Differential Change		4.0 (40 μ V/ $^{\circ}$ C)		2.0 (20 μ V/ $^{\circ}$ C)	mV	$I_C = 3.0$ mA, $V_{CE} = 3.0$ V, $T_A = +25^{\circ}$ C to $+125^{\circ}$ C

ELECTRICAL CHARACTERISTICS (25°C Ambient Temperature unless otherwise noted)

SYMBOL	CHARACTERISTIC	MIN.	MAX.	UNITS	TEST CONDITIONS
h_{FE}	DC Current Gain	20	200		$I_C = 3.0 \text{ mA}, V_{CE} = 1.0 \text{ V}$ $I_C = 3.0 \text{ mA}, V_{CE} = 3.0 \text{ V}$
V_{CB0}	Collector to Base Breakdown Voltage	30		V	$I_C = 1.0 \mu\text{A}, I_E = 0$
V_{EB0}	Emitter to Base Breakdown Voltage	3.0		V	$I_C = 0, I_E = 10 \mu\text{A}$
$V_{CEO(sus)}$	Collector to Emitter Sustaining Voltage (Notes 4 & 6)	15		V	$I_C = 3.0 \text{ mA}, I_B = 0$

Additional Electrical Characteristics on following page.

NOTES:

1. These ratings are limiting values above which the serviceability of any individual semiconductor device may be impaired.
2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
3. These ratings give a maximum junction temperature of 200°C and junction to ambient thermal resistance of 584°C/W (derating factor of 1.72 mW/°C) for one side; 389°C/W (derating factor of 2.57 mW/°C) for both sides; junction to case thermal resistance 290°C/W (derating factor of 3.44 mW/°C) for one side; 145°C/W (derating factor of 6.85 mW/°C) for both sides.
4. Rating refers to a high current point where collector to emitter voltage is lowest.
5. Lowest of two h_{FE} readings is taken as h_{FE1} for purpose of this ratio.
6. Pulse conditions: length = 300 μs ; duty cycle = 1%.
7. For product family characteristic curves, refer to Section 5

ELECTRICAL CHARACTERISTICS (25°C Ambient Temperature unless otherwise noted) (Cont'd)

SYMBOL	CHARACTERISTIC	MIN.	MAX.	UNITS	TEST CONDITIONS
$V_{CE(sat)}$	Collector Saturation Voltage		0.4	V	$I_C = 10 \text{ mA}, I_B = 1.0 \text{ mA}$
$V_{BE(sat)}$	Base Saturation Voltage		1.0	V	$I_C = 10 \text{ mA}, I_B = 1.0 \text{ mA}$
I_{CBO}	Collector Cutoff Current		10	nA	$I_E = 0, V_{CB} = 15 \text{ V}$
I_{EBO}	Emitter Cutoff Current		1.0	μA	$I_C = 0, V_{CB} = 15 \text{ V}, T_A = 150^\circ\text{C}$
h_{fe}	High Frequency Current Gain	6.0	12		$I_C = 4.0 \text{ mA}, V_{CE} = 10 \text{ V}, f = 100 \text{ MHz}$
C_{ob}	Common to Base Output Capacitance		1.7	pF	$I_E = 0, V_{CB} = 10 \text{ V}, f = 140 \text{ kHz}$
C_{ib}	Common to Base Input Capacitance		3.0	pF	$I_E = 0, V_{CB} = 0, f = 140 \text{ kHz}$
$RE(hie)$	Real Part of Common Emitter Input Impedance		2.0	pF	$I_C = 0, V_{EB} = 0.5 \text{ V}, f = 140 \text{ kHz}$
NF	Noise Figure	3.5 (TYP)		dB	$I_C = 3.0 \text{ mA}, V_{CE} = 3.0 \text{ V}, f = 350 \text{ MHz}$
					$I_C = 1.0 \text{ mA}, V_{CE} = 6.0 \text{ V}, f = 60 \text{ MHz}, R_G = 400 \Omega$