

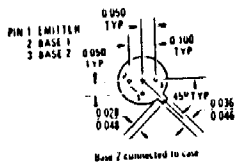
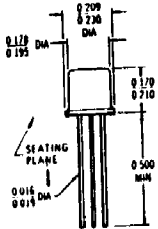
New Jersey Semi-Conductor Products, Inc.

20 STERN AVE.
SPRINGFIELD, NEW JERSEY 07081
U.S.A.

TELEPHONE: (201) 376-2922
(212) 227-6005
FAX: (201) 376-8960

2N3980

Silicon annular PN unijunction transistor designed for military and industrial use in pulse, timing, sensing, and oscillator circuits.



(TO-18 Modified)

MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
RMS Power Dissipation*	P _D	360*	mW
RMS Emitter Current	I _E	50	mA
Peak Pulse Emitter Current**	i _e	1.0**	Amp
Emitter Reverse Voltage	V _{B2E}	30	Volts
Interbase Voltage	V _{B2B1}	35	Volts
Storage Temperature Range	T _{stg}	-65 to +200	°C

* Derate 2.4 mW/°C increase in ambient temperature. Total power dissipation (available power to Emitter and Base-Two) must be limited by external circuitry.

**Capacitance discharge current must fall to 0.37 Amp within 3.0 ms and PRR ≤ 10 PPS.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Intrinsic Standoff Ratio (V _{B2B1} = 10 V) Note 1	η	0.68	—	0.82	—
Interbase Resistance (V _{B2B1} = 3.0 V, I _E = 0)	R _{BB}	4.0	6.0	8.0	k ohms
Interbase Resistance Temperature Coefficient (V _{B2B1} = 3.0 V, I _E = 0, T _A = -65°C to +100°C)	αR _{BB}	0.4	—	0.9	%/°C
Emitter Saturation Voltage (V _{B2B1} = 10 V, I _E = 50 mA) Note 2	V _{EB1(sat)}	—	2.5	3.0	Volts
Modulated Interbase Current (V _{B2B1} = 10 V, I _E = 50 mA)	I _{B2(mod)}	12	15	—	mA
Emitter Reverse Current (V _{B2E} = 30 V, I _{B1} = 0) (V _{B2E} = 30 V, I _{B1} = 0, T _A = 125°C)	I _{EO}	—	5.0	10	nA
Peak Point Emitter Current (V _{B2B1} = 25 V)	I _P	—	0.6	2.0	μA
Valley Point Current (V _{B2B1} = 20 V, R _{B2} = 100 ohms) Note 2	I _V	1.0	4.0	10	mA
Base-One Peak Pulse Voltage (Note 3, Figure 3)	V _{OB1}	6.0	8.0	—	Volts
Maximum Oscillation Frequency (Figure 4)	f(max)	1.0	1.25	—	MHz

NOTES

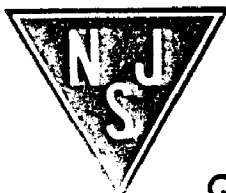
1. Intrinsic standoff ratio, η, is defined by equation:

$$\eta = \frac{V_P - V_{(EB1)}}{V_{B2B1}}$$

Where V_P = Peak Point Emitter Voltage
V_{B2B1} = Interbase Voltage
V_(EB1) = Emitter to Base-One Junction Diode Drop
(~0.5 V @ 10 μA)

2. Use pulse techniques: PW ~ 300 μs duty cycle ≤ 2% to avoid internal heating due to interbase modulation which may result in erroneous readings.

3. Base-One Peak Pulse Voltage is measured in circuit of Figure 3. This specification is used to ensure minimum pulse amplitude for applications in SCR firing circuits and other types of pulse circuits.



Quality Semi-Conductors