

PNP SILICON TRANSISTOR

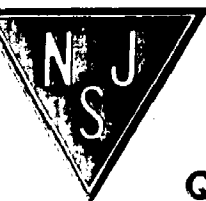
JEDEC TO-39 CASE

MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ )

	<u>SYMBOL</u>		<u>UNITS</u>
Collector-Base Voltage	$V_{CB0}$	80	V
Collector-Emitter Voltage	$V_{CEO}$	80	V
Emitter-Base Voltage	$V_{EBO}$	5.0	V
Collector Current	$I_C$	2.0	A
Power Dissipation	$P_D$	1.25	W
Power Dissipation ( $T_C = 25^\circ\text{C}$ )	$P_D$	8.75	W
Operating and Storage			
Junction Temperature	$T_J, T_{stg}$	-65 to +200	$^\circ\text{C}$
Thermal Resistance	$\theta_{JA}$	140	$^\circ\text{C/W}$
Thermal Resistance	$\theta_{JC}$	20	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

<u>SYMBOL</u>	<u>TEST CONDITIONS</u>	<u>2N4406</u>		<u>2N4407</u>		<u>UNITS</u>
		<u>MIN</u>	<u>MAX</u>	<u>MIN</u>	<u>MAX</u>	
$I_{CB0}$	$V_{CB} = 60\text{V}$		25		25	$\mu\text{A}$
$I_{EBO}$	$V_{EB} = 3.0\text{V}$		25		25	$\mu\text{A}$
$BV_{CEO}$	$I_C = 10\text{mA}$	80		80		V
$BV_{CB0}$	$I_C = 10\mu\text{A}$	80		80		V
$BV_{EBO}$	$I_E = 10\mu\text{A}$	5.0		5.0		V
$V_{CE(SAT)}$	$I_C = 150\text{mA}, I_B = 15\text{mA}$		0.2		0.2	V
$V_{CE(SAT)}$	$I_C = 500\text{mA}, I_B = 50\text{mA}$		0.4		0.4	V
$V_{CE(SAT)}$	$I_C = 1.0\text{A}, I_B = 100\text{mA}$		0.7		0.7	V
$V_{CE(SAT)}$	$I_C = 1.5\text{A}, I_B = 150\text{mA}$		1.5		1.5	V
$V_{BE(SAT)}$	$I_C = 150\text{mA}, I_B = 15\text{mA}$		0.9		0.9	V
$V_{BE(SAT)}$	$I_C = 1.0\text{A}, I_B = 100\text{mA}$		1.3		1.3	V
$V_{BE(SAT)}$	$I_C = 1.5\text{A}, I_B = 150\text{mA}$		1.5		1.5	V
$V_{BE(ON)}$	$V_{CE} = 1.0\text{V}, I_C = 500\text{mA}$		1.0		1.0	V
$h_{FE}$	$V_{CE} = 5.0\text{V}, I_C = 10\text{mA}$	30		80		
$h_{FE}$	$V_{CE} = 5.0\text{V}, I_C = 150\text{mA}$	30		80		
$h_{FE}$	$V_{CE} = 5.0\text{V}, I_C = 500\text{mA}$	30	120	80	240	
$h_{FE}$	$V_{CE} = 5.0\text{V}, I_C = 1.0\text{A}$	20		30		
$h_{FE}$	$V_{CE} = 5.0\text{V}, I_C = 1.5\text{A}$	10		10		
$f_T$	$V_{CE} = 20\text{V}, I_C = 50\text{mA}, f = 100\text{MHz}$	50		50		MHz
$C_{cb}$	$V_{CB} = 10\text{V}, I_E = 0, f = 1.0\text{MHz}$		50		50	pF



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