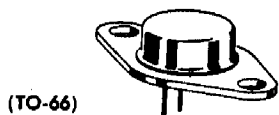


**2N4910 thru 2N4912 (SILICON)**



Medium-power NPN silicon transistors designed for driver circuits, switching, and amplifier applications. Complement to PNP 2N4898 thru 2N4900.

Collector connected to case

**MAXIMUM RATINGS**

Rating	Symbol	2N4910	2N4911	2N4912	Unit
Collector-Emitter Voltage	$V_{CEO}$	40	60	80	Vdc
Collector-Base Voltage	$V_{CB}$	40	60	80	Vdc
Emitter-Base Voltage	$V_{EB}$	← 5.0 →			Vdc
Collector Current - Continuous*	$I_C^*$	← 1.0 →			A <sub>dc</sub>
		← 4.0 →			
Base Current - Continuous	$I_B$	← 1.0 →			A <sub>dc</sub>
Total Device Dissipation $T_C = 25^\circ\text{C}$	$P_D$	← 25 →			Watts
Derate above $25^\circ\text{C}$		← 0.143 →			mW/ $^\circ\text{C}$
Operating & Storage Junction Temperature Range	$T_J, T_{stg}$	← -65 to +200 →			$^\circ\text{C}$

**THERMAL CHARACTERISTICS**

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$\theta_{JC}$	7.0	$^\circ\text{C}/\text{W}$

\* The 1.0 Amp maximum  $I_C$  value is based upon JEDEC current gain requirements.

The 4.0 Amp maximum value is based upon actual current-handling capability of the device (see Figure 5).



2N4910 thru 2N4912 (continued)

ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$  unless otherwise noted)

Characteristic	Fig. No.	Symbol	Min	Max	Unit
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OFF CHARACTERISTICS

Collector-Emitter Sustaining Voltage (1) ( $I_C = 0.1 \text{ A dc}$ , $I_B = 0$ )	-	$BV_{CEO(sus)}$	40 60 80	- - -	Vdc
Collector Cutoff Current ( $V_{CE} = 20 \text{ Vdc}$ , $I_B = 0$ )	-	$I_{CEO}$	-	0.5	mA dc
( $V_{CE} = 30 \text{ Vdc}$ , $I_B = 0$ )	2N4911		-	0.5	
( $V_{CE} = 40 \text{ Vdc}$ , $I_B = 0$ )	2N4912		-	0.5	
Collector Cutoff Current ( $V_{CE} = \text{Rated } V_{CEO}$ , $V_{EB(off)} = 1.5 \text{ Vdc}$ )	12	$I_{CEX}$	-	0.1	mA dc
( $V_{CE} = \text{Rated } V_{CEO}$ , $V_{EB(off)} = 1.5 \text{ Vdc}$ , $T_C = 150^\circ\text{C}$ )			-	1.0	
Collector Cutoff Current ( $V_{CB} = \text{Rated } V_{CB}$ , $I_E = 0$ )	-	$I_{CBO}$	-	0.1	mA dc
Emitter Cutoff Current ( $V_{EB} = 5.0 \text{ Vdc}$ , $I_C = 0$ )	-	$I_{EBO}$	-	1.0	mA dc

ON CHARACTERISTICS (1)

DC Current Gain ( $I_C = 50 \text{ mA dc}$ , $V_{CE} = 1.0 \text{ Vdc}$ )	8	$h_{FE}$	40	-	-
( $I_C = 500 \text{ mA dc}$ , $V_{CE} = 1.0 \text{ Vdc}$ )			20	100	
( $I_C = 1.0 \text{ A dc}$ , $V_{CE} = 1.0 \text{ Vdc}$ )			10	-	
Collector-Emitter Saturation Voltage ( $I_C = 1.0 \text{ A dc}$ , $I_B = 0.1 \text{ A dc}$ )	9 11 13	$V_{CE(sat)}$	-	0.6	Vdc
Base-Emitter Saturation Voltage ( $I_C = 1.0 \text{ A dc}$ , $I_B = 0.1 \text{ A dc}$ )	11 13	$V_{BE(sat)}$	-	1.3	Vdc
Base-Emitter On Voltage ( $I_C = 1.0 \text{ A dc}$ , $V_{CE} = 1.0 \text{ Vdc}$ )	11 13	$V_{BE(on)}$	-	1.3	Vdc

SMALL SIGNAL CHARACTERISTICS

Current-Gain - Bandwidth Product ( $I_C = 250 \text{ mA dc}$ , $V_{CE} = 10 \text{ Vdc}$ , $f = 1.0 \text{ MHz}$ )	-	$f_T$	3.0	-	MHz
Output Capacitance ( $V_{CB} = 10 \text{ Vdc}$ , $I_E = 0$ , $f = 100 \text{ kHz}$ )	-	$C_{ob}$	-	100	pF
Small-Signal Current Gain ( $I_C = 250 \text{ mA dc}$ , $V_{CE} = 10 \text{ Vdc}$ , $f = 1.0 \text{ kHz}$ )	-	$h_{fe}$	25	-	

(1) Pulse Test: PW = 300  $\mu\text{s}$ , Duty Cycle = 2.0%

