

# 2N5330

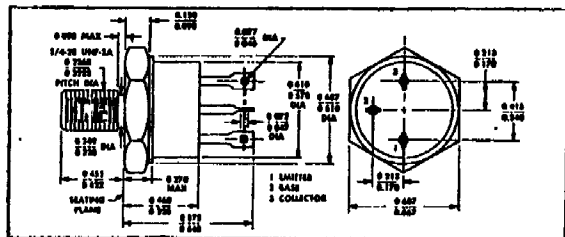
## 30 AMP

### HIGH SPEED NPN TRANSISTOR

### 150 VOLTS

JEDEC TO-61

ALL TERMINALS ISOLATED FROM CASE



#### FEATURES

- RADIATION TOLERANT
- FAST SWITCHING, 350 NSEC MAX  $t_{on}$
- HIGH FREQUENCY, TYPICAL  $f_T$  100 MHZ
- BVCEO 90 VOLTS MIN, TYPICALLY 150 VOLTS
- HIGH LINEAR GAIN, LOW SATURATION VOLTAGE
- 200°C OPERATING, GOLD EUTECTIC DIE ATTACH
- DESIGNED FOR COMPLEMENTARY USE WITH SPT5330

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	$V_{CE0}$	90	Volts
Collector - Base Voltage	$V_{CB0}$	150	Volts
Emitter - Base Voltage	$V_{EB0}$	8	Volts
Collector Current	$I_C$	30	Amps
Base Current	$I_B$	5	Amps
Total Device Dissipation @ $T_C = 100^\circ C$	$P_D$	80	Watts
Derate above 100 °C		800	mW/°C
Operating and Storage Temperature	$T_j, T_{stg}$	-65 to +200	°C

#### THERMAL CHARACTERISTICS

Characteristics	Symbol	Value	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.25	°C/W

#### ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Min.	Max.	Unit
Collector - Emitter Breakdown Voltage* ( $I_C = 100$ mA dc)	$BV_{CE0}$	90		Vdc
Collector - Base Breakdown Voltage ( $I_C = 200$ $\mu$ A dc)	$BV_{CB0}$	150		Vdc
Emitter - Base Breakdown Voltage ( $I_E = 200$ $\mu$ A dc)	$BV_{EB0}$	8		Vdc

## ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Min.	Max.	Unit
Collector Cutoff Current ( $V_{CE} = 150$ Vdc, $V_{BE} = 500$ mVdc)	$I_{CEV}$		10**	mAdc
Collector Cutoff Current ( $V_{CE} = 150$ Vdc, $V_{BE} = 500$ mVdc, $T_C = 150^\circ\text{C}$ )	$I_{CEV}$		50***	mAdc
Emitter Cutoff Current ( $V_{EB} = 8$ Vdc)	$I_{EBO}$		5	mAdc
DC Current Gain* ( $I_C = 10$ Adc, $V_{CE} = 2$ Vdc) ( $I_C = 30$ Adc, $V_{CE} = 3$ Vdc)	$h_{FE}$	40 10	120 50	
Collector - Emitter Saturation Voltage* ( $I_C = 10$ Adc, $I_B = 500$ mAdc) ( $I_C = 30$ Adc, $I_B = 3$ Adc)	$V_{CE(SAT)}$		0.6 1.8	Vdc
Base - Emitter Saturation Voltage* ( $I_C = 10$ Adc, $I_B = 300$ mAdc) ( $I_C = 30$ Adc, $I_B = 3$ Adc)	$V_{BE(SAT)}$		1.3 1.8	Vdc
Current - Gain - Bandwidth Product ( $I_C = 3$ Adc, $V_{CE} = 10$ Vdc, $f = 10$ MHz)	$f_T$	80		MHz
Output Capacitance ( $V_{CB} = 10$ Vdc, $f = 1$ MHz)	$C_{ob}$		500	pf
Input Capacitance ( $V_{BE} = 1.0$ Vdc, $f = 1$ MHz)	$C_{ib}$		1250	pf
Delay Time	$(t_{on})$  $(t_{off})$	$t_d$ +	350	ns
Rise Time				
Storage Time				
Fall Time				
$V_{CC} = 21$ Vdc. $I_C = 10$ Adc, $I_{B1} = I_{B2} = 500$ mAdc		$t_s$ +	1.25	us

\*Pulse Test: Pulse width = 300 us, Duty Cycle = 2%    \*\*Typically 1 uAdc    \*\*\*Typically 50 uAdc

### TYPICAL OPERATING CURVES

