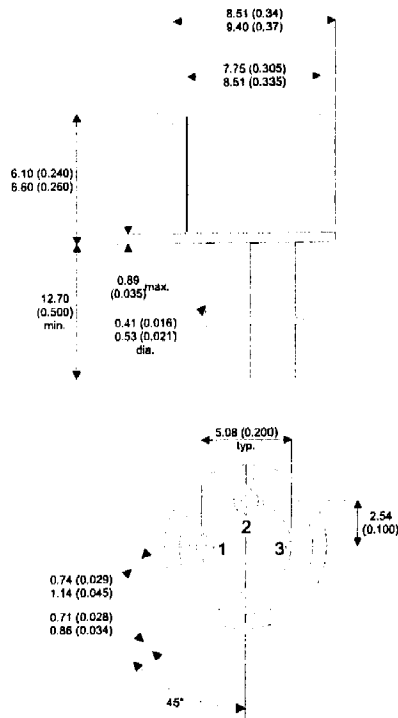


2N6190

MECHANICAL DATA

Dimensions in mm(Inches)



TO39 PACKAGE

Pin 1 = Emitter Pin 2 = Base Pin 3 = Collector

PNP SILICON TRANSISTORS

FEATURES

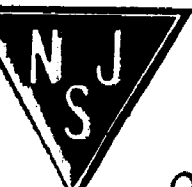
- SILICON PLANAR EPITAXIAL PNP TRANSISTOR
- HERMETICALLY SEALED TO-39 PACKAGE
- CECC LEVEL SCREENING OPTIONS

APPLICATIONS:

Hermetically sealed, the 2N6190 silicon planar epitaxial PNP transistor is intended for general purpose applications.

ABSOLUTE MAXIMUM RATINGS $T_{CASE} = 25^{\circ}C$ unless otherwise stated

V_{CBO}	Collector – Base Voltage ($I_E = 0$)	80V
V_{CEO}	Collector – Emitter Voltage ($I_B = 0$)	80V
V_{EBO}	Emitter – Base Voltage ($I_C = 0$)	6V
I_C	Collector Current	5A
I_B	Base Current	1A
P_{tot}	Total Dissipation at $T_C \leq 25^{\circ}C$ derate above $25^{\circ}C$	10W 17.5°C/W
T_{stg}	Storage Temperature Range	-55 to +200°C
T_j	Junction temperature	200°C



NJ Semi-Conductors reserves the right to change test conditions, parameters limits and package dimensions without notice information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

ELECTRICAL CHARACTERISTICS ($T_{case} = 25^{\circ}C$ unless otherwise stated)

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{(BR)CEO}^*$	Collector Emitter Breakdown Voltage $I_C = 50mA$	80			V
I_{CBO}	Collector-Base Cut Off Current $I_E = 0$ $V_{CB} = 80V$			10	μA
I_{CEX}	Collector-Emitter Cut Off Current $V_{BE} = 1.5V$ $V_{CE} = 75V$ $T_A = 150^{\circ}C$			10	μA
I_{CEO}	Collector-Emitter Cut Off Current $I_B = 0$ $V_{CE} = 75V$			1.0	mA
I_{EBO}	Collector-Emitter Cut Off Current $V_{BE} = 6V$			100	μA
$V_{CE(sat)}^*$	Collector Emitter Saturation Voltage $I_C = 2A$ $I_B = 0.2A$ $I_C = 5A$ $I_B = 0.5A$			0.7	V
$V_{BE(sat)}^*$	Base Emitter Voltage $I_C = 2A$ $I_B = 0.2A$ $I_C = 5A$ $I_B = 0.5A$			1.2	V
h_{FE}^*	DC Current Gain $I_C = 0.5A$ $V_{CE} = 2V$ $I_C = 2A$ $V_{CE} = 2V$ $I_C = 5A$ $V_{CE} = 2V$	30		120	—
f_T	Transition Frequency $V_{CE} = 10V$ $I_C = 0.5A$ $f = 10MHz$	30			MHz
C_{IBO}	Input Capacitance, Output Open Circuited $V_{BE} = 2V$ $I_C = 0$ $f = 100kHz$			1250	pF
C_{OBO}	Open Circuit Output Capacitance $V_{CB} = 10V$ $I_E = 0$ $f = 100kHz$			300	
t_d	Delay Time $V_{CC} = 40V$ $I_E = 2.0A$			100	ns
t_r	Rise Time $V_{BE(off)} = 3.0$ $I_{B1} = 0.2A$			100	ns
t_s	Storage Time $V_{CC} = 40V$ $I_E = 2.0A$			20	μs
t_f	Fall Time $I_{B1} = I_{B2} = 0.2A$			200	ns

* Pulse Test: $t_p = 300\mu s$, $\delta = 1\%$.