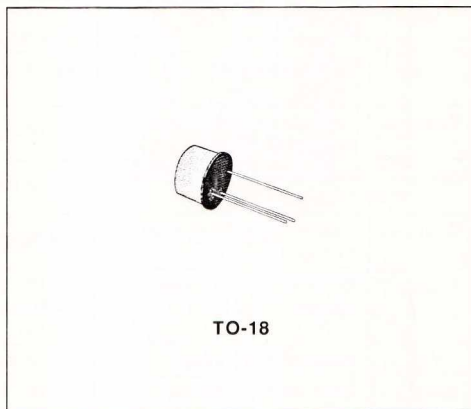


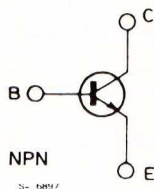
HIGH-VOLTAGE, HIGH-CURRENT AMPLIFIER

DESCRIPTION

The BFR18 is a silicon planar epitaxial NPN transistor in Jedec TO-18 metal case. This device is designed for amplifier applications over a wide range of voltage and current.



INTERNAL SCHEMATIC DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CES}	Collector-emitter Voltage ($V_{BE} = 0$)	85	V
V_{CEO}	Collector-emitter Voltage ($I_B = 0$)	55	V
V_{EBO}	Emitter-base Voltage ($I_C = 0$)	7	V
I_C	Collector Current	1	A
P_{tot}	Total Power Dissipation at $T_{amb} \leq 25^\circ\text{C}$ at $T_{case} \leq 25^\circ\text{C}$	0.5	W
		1.8	W
T_{stg}, T_J	Storage and Junction Temperature	- 55 to 200	$^\circ\text{C}$

THERMAL DATA

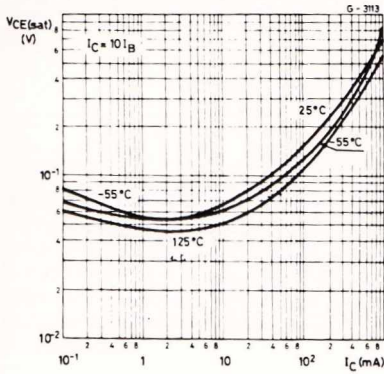
$R_{th\ j-case}$	Thermal Resistance Junction–case	Max	97	$^{\circ}C/W$
$R_{th\ j-amb}$	Thermal Resistance Junction–ambient	Max	350	$^{\circ}C/W$

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\ ^{\circ}C$ unless otherwise specified)

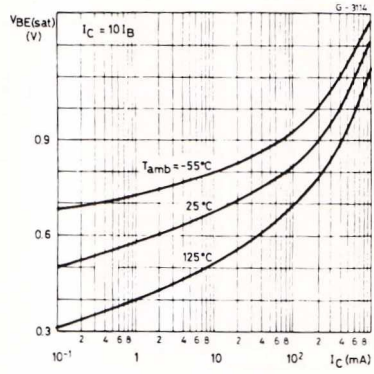
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{CES}	Collector Cutoff Current ($V_{BE} = 0$)	$V_{CE} = 60\ V$	$T_{amb} = 150\ ^{\circ}C$	0.2	10	nA
		$V_{CE} = 60\ V$		0.2	10	μA
I_{EBO}	Emitter Cutoff Current ($I_C = 0$)	$V_{EB} = 5\ V$		0.1	10	nA
$V_{(BR)\ CES}$	Collector–emitter Breakdown Voltage ($V_{BE} = 0$)	$I_C = 100\ \mu A$	85			V
$V_{(BR)\ CEO}^*$	Collector–emitter Breakdown Voltage ($I_B = 0$)	$I_C = 30\ mA$	55			V
$V_{(BR)\ EBO}$	Emitter–base Breakdown Voltage ($I_C = 0$)	$I_E = 100\ \mu A$	7			V
$V_{CE(sat)}^*$	Collector–emitter Saturation Voltage	$I_C = 150\ mA$ $I_B = 15\ mA$		0.13	0.25	V
		$I_C = 500\ mA$ $I_B = 50\ mA$		0.3		V
		$I_C = 1\ A$ $I_B = 0.1\ A$		0.65	1	V
V_{BE}^*	Base–emitter Voltage	$I_C = 10\ mA$ $V_{CE} = 1\ V$		0.66		V
$V_{BE(sat)}^*$	Base–emitter Saturation Voltage	$I_C = 150\ mA$ $I_B = 15\ mA$		0.85	1	V
		$I_C = 500\ mA$ $I_B = 50\ mA$		1.1		V
		$I_C = 1\ A$ $I_B = 0.1\ A$		1.35	1.6	V
h_{FE}^*	DC Current Gain	$I_C = 100\ \mu A$ $V_{CE} = 1\ V$	30	75		
		$I_C = 10\ mA$ $V_{CE} = 1\ V$	70	120	180	
		$I_C = 150\ mA$ $V_{CE} = 1\ V$	60	90	180	
		$I_C = 500\ mA$ $V_{CE} = 1\ V$	30	45		
		$I_C = 150\ mA$ $V_{CE} = 1\ V$ $T_{amb} = -55\ ^{\circ}C$	15			
h_{fe}	Small Signal Current Gain	$I_C = 1\ mA$ $V_{CE} = 5\ V$ $f = 1\ kHz$		120		
f_T	Transition Frequency	$I_C = 50\ mA$ $V_{CE} = 10\ V$ $f = 20\ MHz$	60	90		MHz
C_{EBO}	Emitter–base Capacitance	$I_C = 0$ $V_{EB} = 0.5\ V$ $f = 1\ MHz$		50	80	pF
C_{CBO}	Collector–base Capacitance	$I_E = 0$ $V_{CB} = 10\ V$ $f = 1\ MHz$		12	20	pF
NF	Noise Figure	$I_C = 30\ \mu A$ $V_{CE} = 10\ V$ $R_g = 1\ k\Omega$ $f = 1\ kHz$		2	8	dB
h_{ie}	Input Impedance	$I_C = 1\ mA$ $V_{CE} = 5\ V$ $f = 1\ kHz$		2.2		k Ω
h_{re}	Reverse Voltage Ratio	$I_C = 1\ mA$ $V_{CE} = 5\ V$ $f = 1\ kHz$		2.4×10^{-4}		
h_{oe}	Output Admittance	$I_C = 1\ mA$ $V_{CE} = 5\ V$ $f = 1\ kHz$		8.5		μS

* Pulsed : pulse duration = 300 μs , duty cycle = 1%.

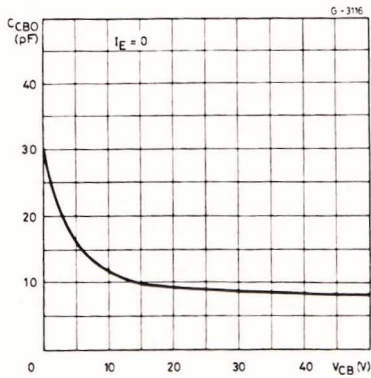
Collector-emitter Saturation Voltage.



Base-emitter Saturation Voltage.



Collector-base Capacitance.



High Frequency Current Gain.

