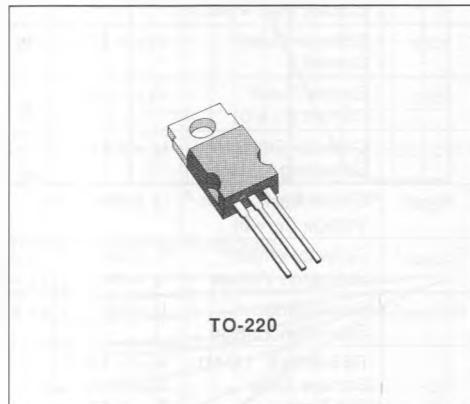
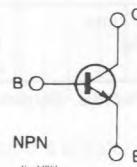


FAST NPN SWITCHING TRANSISTOR

- VERY LOW SATURATION VOLTAGE
- FAST TURN-OFF AND TURN-ON



INTERNAL SCHEMATIC DIAGRAM



DESCRIPTION

High speed transistor suited for low voltage applications.

High frequency and efficiency converters switching regulators motor control.

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CBO}	Collector-base Voltage ($I_E = 0$)	400	V
V_{CEO}	Collector-emitter Voltage ($I_B = 0$)	200	V
V_{EBO}	Emitter-base Voltage ($I_C = 0$)	7	V
I_C	Collector Current	10	A
I_{CM}	Collector Peak Current	15	A
I_B	Base Current	2	A
I_{BM}	Base Peak Current	4	A
P_{tot}	Total Dissipation at $T_c < 25^\circ\text{C}$	85	W
P_{tot}	Total Dissipation at $T_c < 60^\circ\text{C}$	65	W
T_{stg}	Storage Temperature	-65 to 175	$^\circ\text{C}$
T_J	Max. Operating Junction Temperature	175	$^\circ\text{C}$

THERMAL DATA

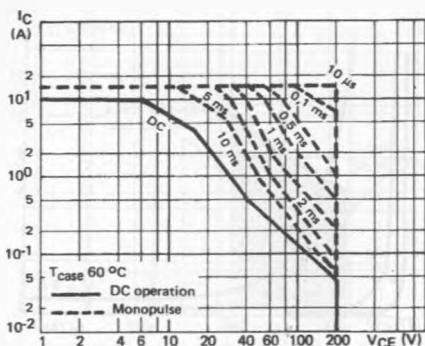
$R_{\text{thj-case}}$	Thermal Resistance Junction-case	Max	1.76	$^{\circ}\text{C/W}$
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ELECTRICAL CHARACTERISTICS ($T_{\text{case}} = 25^{\circ}\text{C}$ unless otherwise specified)

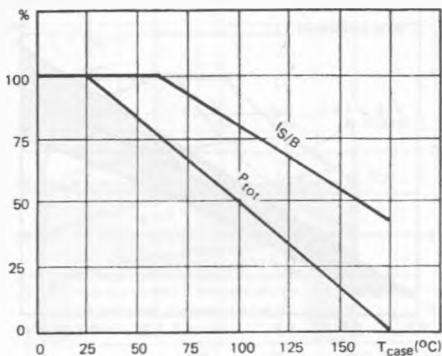
Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Unit
I_{CER}	Collector Cutoff Current ($R_{\text{BE}} = 50\Omega$)	$V_{\text{CE}} = 400\text{V}$	$T_c = 125^{\circ}\text{C}$			3	mA
I_{EX}	Collector Cutoff Current	$V_{\text{CE}} = 400\text{V}$	$V_{\text{BE}} = -1.5\text{V}$	$T_c = 125^{\circ}\text{C}$		1	mA
I_{EBO}	Emitter Cutoff Current ($I_C = 0$)	$V_{\text{EB}} = 5\text{V}$				1	mA
$V_{\text{CEO(sus)}}^*$	Collector Emitter Sustaining Voltage	$I_C = 0.2\text{A}$	$L = 25\text{mH}$		200		V
V_{EBO}	Emitter-base Voltage ($I_C = 0$)	$I_E = 50\text{mA}$		7		30	V
$V_{\text{CE(sat)}}^*$	Collector-emitter Saturation Voltage	$I_C = 3\text{A}$	$I_B = 0.3\text{A}$			0.7	V
$V_{\text{BE(sat)}}^*$	Base-emitter Saturation Voltage	$I_C = 6\text{A}$	$I_B = 0.6\text{A}$			1.5	V
t_{on} t_s t_f	RESISTIVE LOAD Storage Time Fall Time Turn-on Time	$V_{\text{CC}} = 150\text{V}$	$I_C = 6\text{A}$		0.3	1	μs
		$V_{\text{BE}} = -6\text{V}$	$I_{B1} = 0.6\text{A}$		0.5	1.5	μs
		$R_{\text{BB}} = 5\Omega$			0.1	0.25	μs
t_s t_f	INDUCTIVE LOAD Storage Time Fall Time	$V_{\text{CC}} = 150\text{V}$	$I_C = 6\text{A}$		1	0.04	μs
		$I_{B1} = 0.6\text{A}$	$V_{\text{BE}} = -5\text{V}$				μs
t_s t_f	Storage Time Fall Time	$L_B = 1\mu\text{H}$					μs
t_s t_f	Storage Time Fall Time	$V_{\text{CC}} = 150\text{V}$	$I_C = 6\text{A}$			3	μs
		$I_{B1} = 0.6\text{A}$	$V_{\text{BE}} = -5\text{V}$			0.2	μs
		$L_B = 1\mu\text{H}$					

* Pulsed : Pulse duration = 300 μs , duty cycle = 2%.

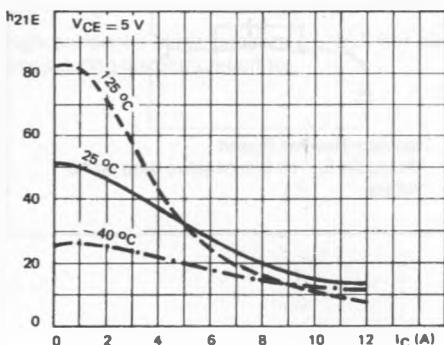
DC and Pulse Area.



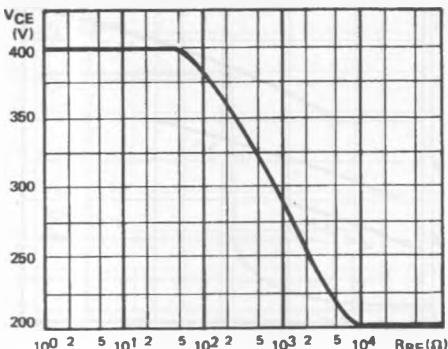
Power and I_{SB} Derating vs. Case Temperature.



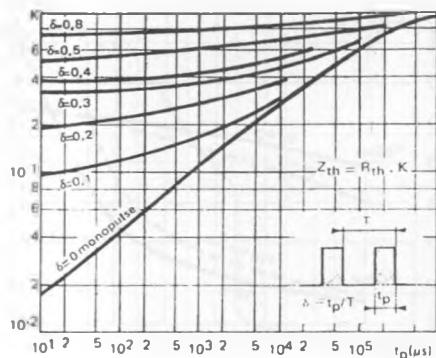
DC Current Gain.



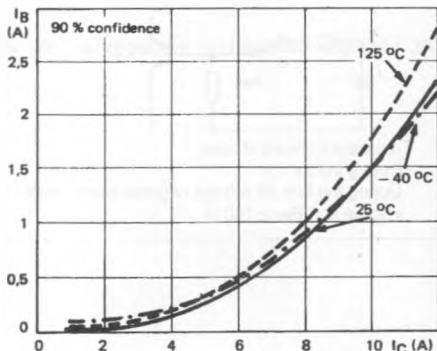
Collector-emitter Voltage vs. Base-emitter Resistance.



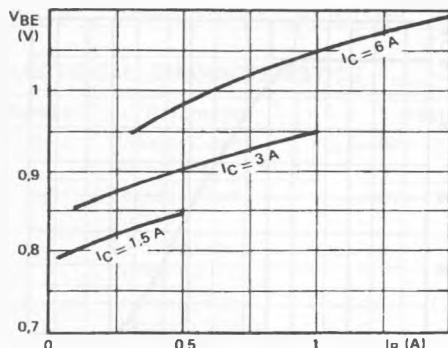
Transient Thermal Response.



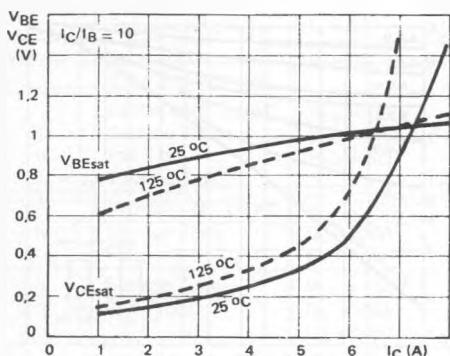
Minimum Base Current to saturate the Transistor.



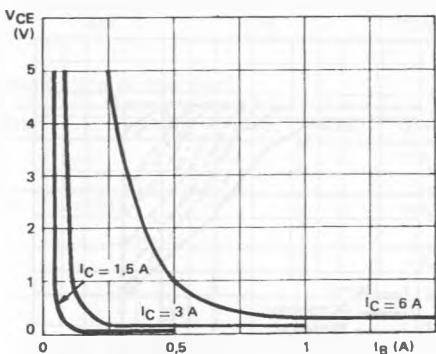
Base Characteristics.



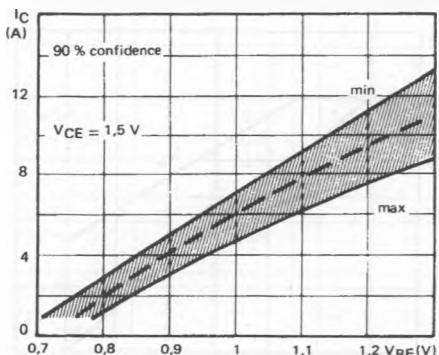
Saturation Voltage.



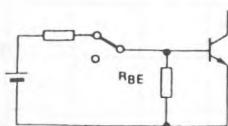
Collector Saturation Region.



Collector Current Spread vs Base-emitter Voltage.

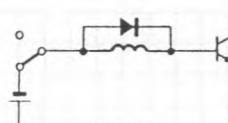


SWITCHING OPERATING AND OVERLOAD AREAS



Transistor Forward Biased

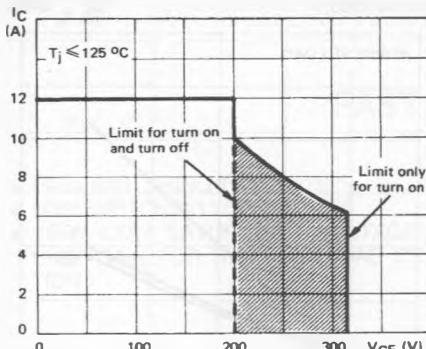
- During the turn on
- During the turn off without negative base-emitter voltage and $R_{BE} \geq 100 \Omega$



Transistor Reverse Biased

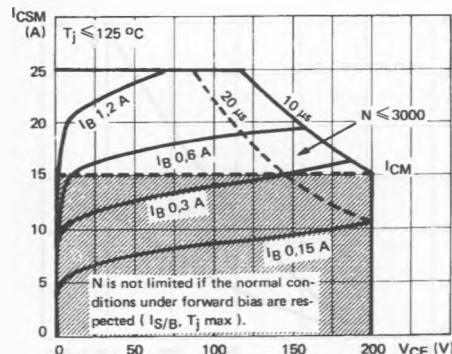
- During the turn off with negative base emitter voltage

Forward Biased Safe Operating Area (FBSOA).



The hatched zone can only be used for turn on.

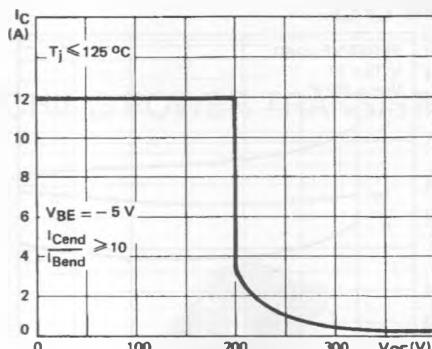
Forward Biased Accidental Overload Area (FBAOA).



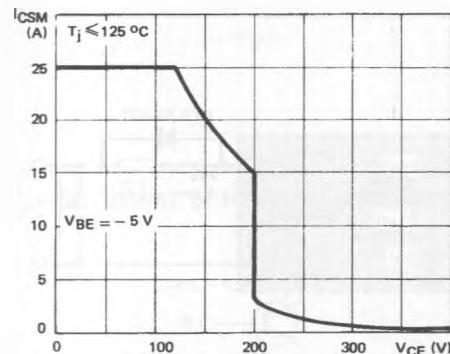
The Kellogg network (heavy point) allows the calculation of the maximum value of the short-circuit current for a given base current I_B (90 % confidence).

High accidental surge currents ($I > I_{CM}$) are allowed if they are non repetitive and applied less than 3000 times during the component life.

Reverse Biased Safe Operating Area (RBSOA).

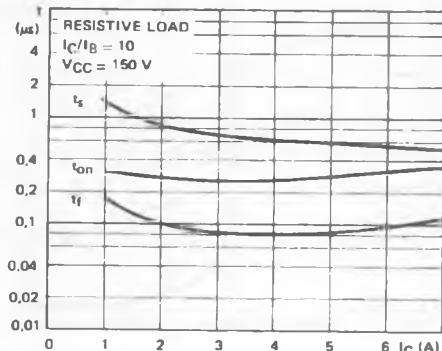


Reverse Biased Accidental Overload Area (RBAOA).

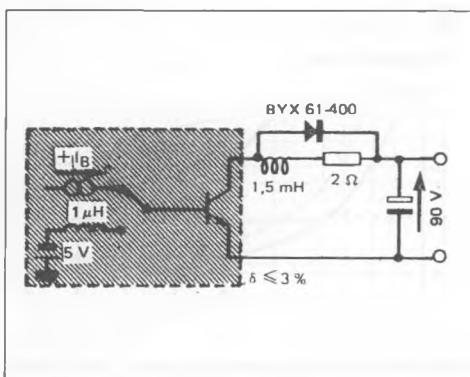


After the accidental overload current, the RBAOA has to be used for the turn off.

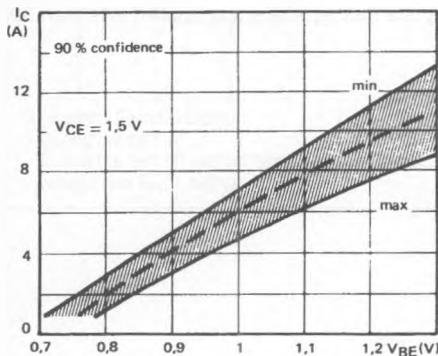
Switching Times vs Collector Current (resistive load).



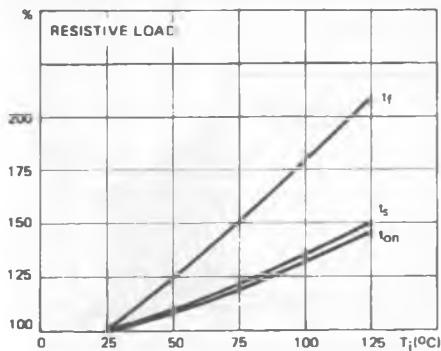
Switching Times Test Circuit on Inductive Load.



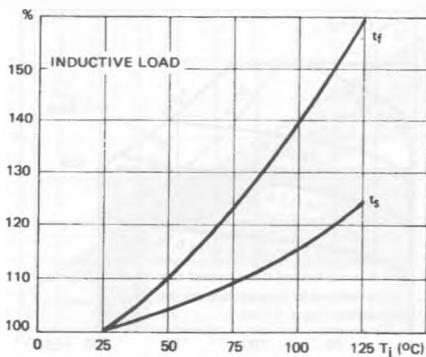
Switching Times vs Collector Current.



Switching Times vs Junction Temperature (resistive load).



Switching Times vs Junction Temperature (inductive load).



Fall Times vs Applied Voltage Slope.

