

CMKT5078
 CMKT5087
 CMKT5088

ULTRAmi™
 SURFACE MOUNT
 SILICON TRANSISTORS

ULTRAmi™



SOT-363 CASE

Central™
Semiconductor Corp.

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMKT5078 , CMKT5087, and CMKT5088, are Silicon transistors in an ULTRAmi™ surface mount package, designed for applications requiring high gain and low noise.

The following configurations are available:

CMKT5078 Dual, Complementary Marking Code: K78
 CMKT5087 Dual, PNP Marking Code: K87
 CMKT5088 Dual, NPN Marking Code: K88

MAXIMUM RATINGS: (T_A=25°C)

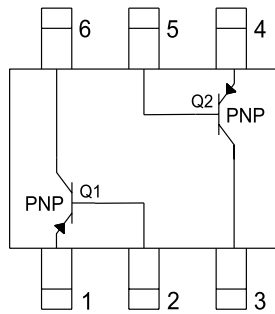
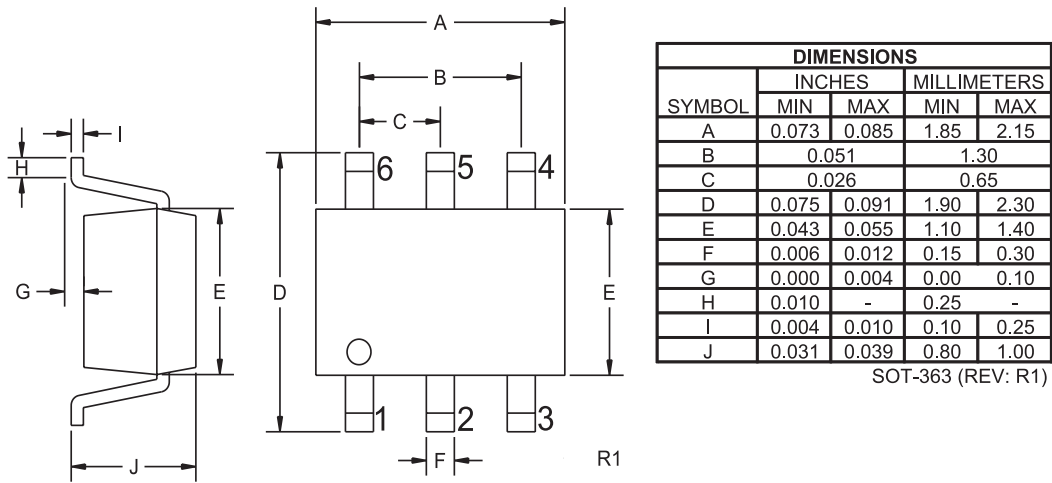
	SYMBOL	NPN	PNP	UNITS
Collector-Base Voltage	V _{CBO}	35	50	V
Collector-Emitter Voltage	V _{CEO}	30	50	V
Emitter-Base Voltage	V _{EBO}	4.5	3.0	V
Collector Current	I _C		50	mA
Power Dissipation	P _D		350	mW
Operating and Storage Junction Temperature	T _J , T _{stg}		-65 to +150	°C
Thermal Resistance	θ _{JA}		357	°C/W

ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

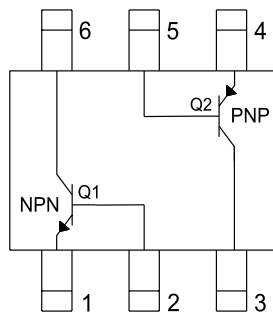
SYMBOL	TEST CONDITIONS	NPN		PNP		UNITS
		MIN	MAX	MIN	MAX	
I _{CBO}	V _{CB} =10V				10	nA
I _{CBO}	V _{CB} =35V				50	nA
I _{CBO}	V _{CB} =20V		50			nA
I _{EBO}	V _{EB} =3.0V		50			nA
BV _{CBO}	I _C =100µA	35		50		V
BV _{CEO}	I _C =1.0mA	30		50		V
BV _{EBO}	I _E =100µA	4.5		3.0		V
V _{CE(SAT)}	I _C =10mA, I _B =1.0mA		0.50		0.30	V
V _{BE(SAT)}	I _C =10mA, I _B =1.0mA		0.80		0.85	V
h _{FE}	V _{CE} =5.0V, I _C =0.1mA	300	900	250	800	
h _{FE}	V _{CE} =5.0V, I _C =1.0mA	350		250		
h _{FE}	V _{CE} =5.0V, I _C =10mA	300		250		
f _T	V _{CE} =5.0V, I _C =500µA, f=20MHz	50		40		MHz
C _{ob}	V _{CB} =5.0V, I _E =0, f=1.0MHz		4.0		4.0	pF
C _{ib}	V _{BE} =0.5V, I _C =0, f=1.0MHz		15			pF
h _{fe}	V _{CE} =5.0V, I _C =1.0mA, f=1.0kHz	350	1400	250	900	
NF	V _{CE} =5.0V, I _C =100µA, R _S =10kΩ f=10Hz to 15.7kHz		3.0		2.0	dB
NF	V _{CE} =5.0V, I _C =100µA, R _S =10kΩ f=1.0kHz				2.0	dB

R1 (21-February 2002)

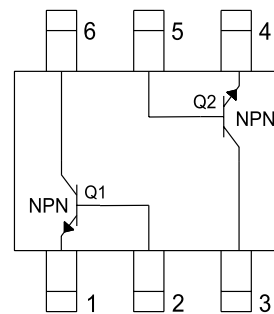
SOT-363 CASE - MECHANICAL OUTLINE



CMKT5087
Marking Code: K87



CMKT5078
Marking Code: K78



CMKT5088
Marking Code: K88

LEAD CODE (for all devices above):

- 1) Emitter Q1
- 2) Base Q1
- 3) Collector Q2
- 4) Emitter Q2
- 5) Base Q2
- 6) Collector Q1