

IRF510
IRF511
IRF512
IRF513



**N-Channel Enhancement-Mode
Vertical DMOS Power FETs**

Ordering Information

BV _{DSS} / BV _{DGS}	R _{DS(ON)} (max)	I _{D(ON)} (min)	Order Number / Package
			TO-220
100V	0.6Ω	4.0A	IRF510
60V	0.6Ω	4.0A	IRF511
100V	0.8Ω	3.5A	IRF512
60V	0.8Ω	3.5A	IRF513

Features

- Freedom from secondary breakdown
- Low power drive requirement
- Ease of paralleling
- Low C_{ISS} and fast switching speeds
- Excellent thermal stability
- Integral Source-Drain diode
- High input impedance and high gain
- Complementary N- and P-Channel devices

Applications

- Motor control
- Converters
- Amplifiers
- Switches
- Power supply circuits
- Drivers (Relays, Hammers, Solenoids, Lamps, Memories, Displays, Bipolar Transistors, etc.)

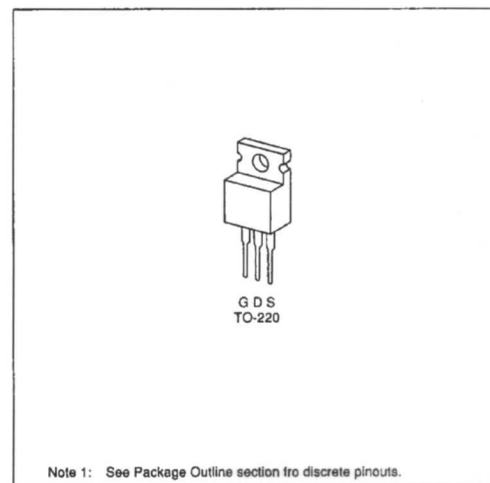
Absolute Maximum Ratings

Drain-to-Source Voltage	BV _{DSS}
Drain-to-Gate Voltage	BV _{DGS}
Gate-to-Source Voltage	± 20V
Operating and Storage Temperature	-55°C to +150°C
Soldering Temperature*	300°C

*Distance of 1.6 mm from case for 10 seconds.

Package Options

(Note 1)



NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

Thermal Characteristics

Package	I_D (continuous)*	I_D (pulsed)*	Power Dissipation @ $T_C = 25^\circ\text{C}$	θ_{JC} $^\circ\text{C/W}$	θ_{JA} $^\circ\text{C/W}$	I_{DR}	I_{DRM}^*
IRF510	4.0A	16.0A	20W	80	6.4	4.0A	16.0A
IRF511	4.0A	16.0A	20W	80	6.4	4.0A	16.0A
IRF512	3.5A	14.0A	20W	80	6.4	3.5A	14.0A
IRF513	3.5A	14.0A	20W	80	6.4	3.5A	14.0A

* I_D (continuous) is limited by max rated T_J .

Electrical Characteristics (@ 25°C unless otherwise specified)

(Notes 1 and 2)

Symbol	Parameter	Min	Typ	Max	Unit	Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	100			V	$V_{GS} = 0, I_D = 250\mu\text{A}$
$V_{GS(th)}$	Gate Threshold Voltage	2.0		4.0	V	$V_{GS} = V_{DS}, I_D = 250\mu\text{A}$
I_{GSS}	Gate Body Leakage			500	nA	$V_{GS} = \pm 20V, V_{DS} = 0$
I_{DSS}	Zero Gate Voltage Drain Current			250	μA	$V_{GS} = 0, V_{DS} = \text{Max Rating}$
				1000	μA	$V_{GS} = 0, V_{DS} = 0.8 \text{ Max Rating}$ $T_C = 125^\circ\text{C}$
$I_{D(ON)}$	ON-State Drain Current	4.0			A	$V_{GS} = 10V$ $V_{DS} > I_{D(ON)} \times R_{DS(ON)}$ Max Rating
$R_{DS(ON)}$	Static Drain-to-Source ON-State Resistance			0.6	Ω	$V_{GS} = 10V, I_D = 2.0A$
				0.8	Ω	
G_{FS}	Forward Transconductance	1.0	1.5		S	$V_{DS} > I_{D(ON)} \times R_{DS(ON)}$ Max Rating $I_D = 2.0A$
C_{ISS}	Input Capacitance			150	pF	$V_{GS} = 0, V_{DS} = 25V$
C_{OSS}	Common Source Output Capacitance			100	pF	$f = 1 \text{ MHz}$
C_{RSS}	Reverse Transfer Capacitance			25	pF	
$t_{d(ON)}$	Turn-ON Delay Time			20	ns	$V_{DD} = 0.5BV_{DSS}$ $I_D = 2.0A$ $R_S = 50\Omega$
t_r	Rise Time			25	ns	
$t_{d(OFF)}$	Turn-OFF Delay Time			25	ns	
t_f	Fall Time			20	ns	
V_{SD}	Diode Forward Voltage Drop			2.5	V	
				2.0	V	$V_{GS} = 0, I_{SD} = 3.5A$
t_{rr}	Reverse Recovery Time		230		ns	$T_J = 150^\circ\text{C}, I_{SD} = 4.0A,$ $dI_{F/dt} = 100A/\mu\text{S}$

Note 1: All D.C. parameters 100% tested at 25°C unless otherwise stated. (Pulse test: 300 μs pulse, 2% duty cycle.)

Note 2: All A.C. parameters sample tested.

Switching Waveforms and Test Circuit

